

Model Name: GA-H97-DS3H

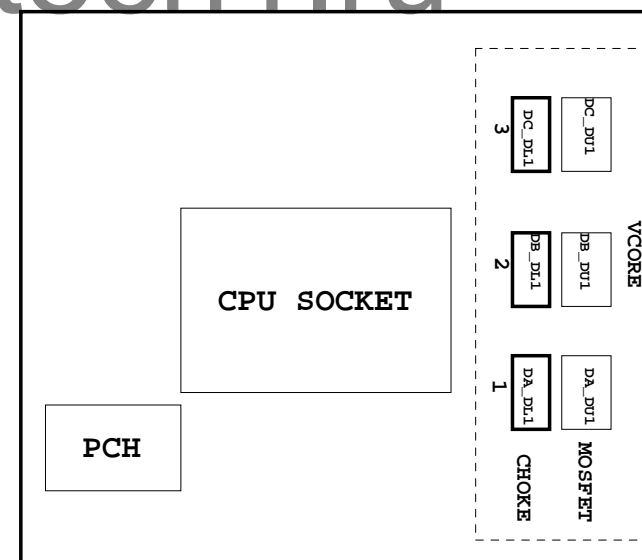
1.0

SHEET TITLE

01	COVER SHEET
02	BOM & PCB MODIFY HISTORY
03	BLOCK DIAGRAM
04	CPU_LGA1150-A
05	CPU_LGA1150-B
06	CPU_LGA1150-C
07	DDR III CHANNEL A
08	DDR III CHANNEL B
09	PCH_FDI,DMI,USB,PCIE
10	PCH_RGB,CLK BUFFER
11	PCH_HOST,SATA,PCI
12	PCH_GPIO,CTRL,AUDIO
13	PCH_PWR,GND
14	PCI EXPRESS*16 SLOT
15	PCIEX1*2 , PCIEX4 SLOT
16	ITE8892 PCI BRIDGE
17	PCI SLOT 1&2
18	I/O ITE8620
19	COM, -PROHOT, R_USB
20	Dual BIOS / LPT
21	ALC887-VD2 CODEC
22	REAR AUDIO JACK
23	VCORE_ ISL95820_1
24	VCORE_ ISL95820_2
25	DDR15V / M3 POWER
26	NCP3933 OVER VOLTAGE
27	DISCRETE POWER

SHEET TITLE

28	F_PANEL , F_USB2.0/3.0
29	ATX POWER, CLOCK GEN
30	HWM , KB/MS , FAN CTRL
31	Realtek 8111F-VL
32	DVI
33	HDMI
34	TABLE LIST
35	
36	
37	
38	
39	
40	



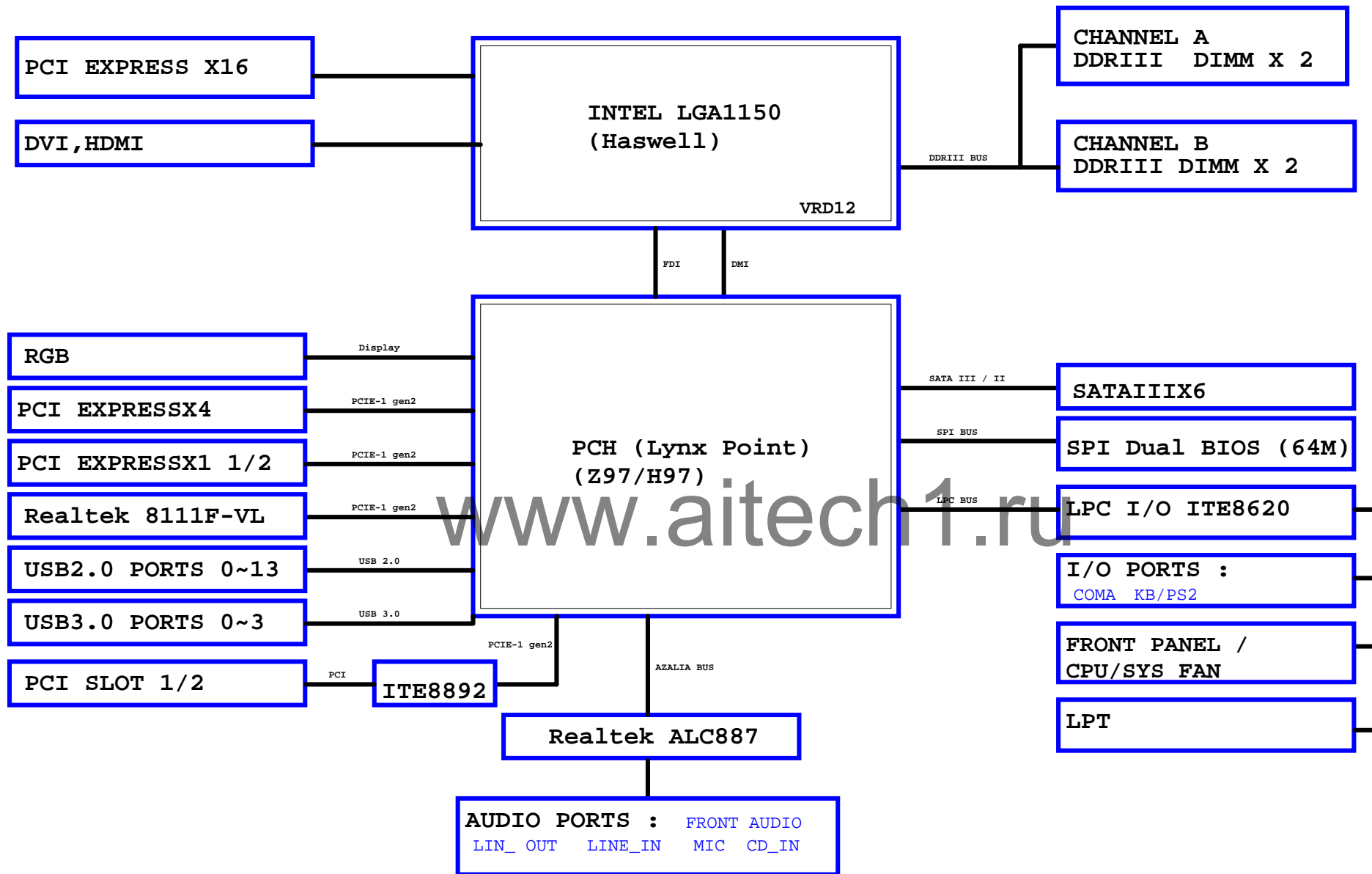
Gigabyte Technology			
Title Cover Sheet			
Size Custom	Document Number GA-H97-DS3H	Rev 1.0	
Date: Monday, March 10, 2014	Sheet 1 of 34		

Component value change history

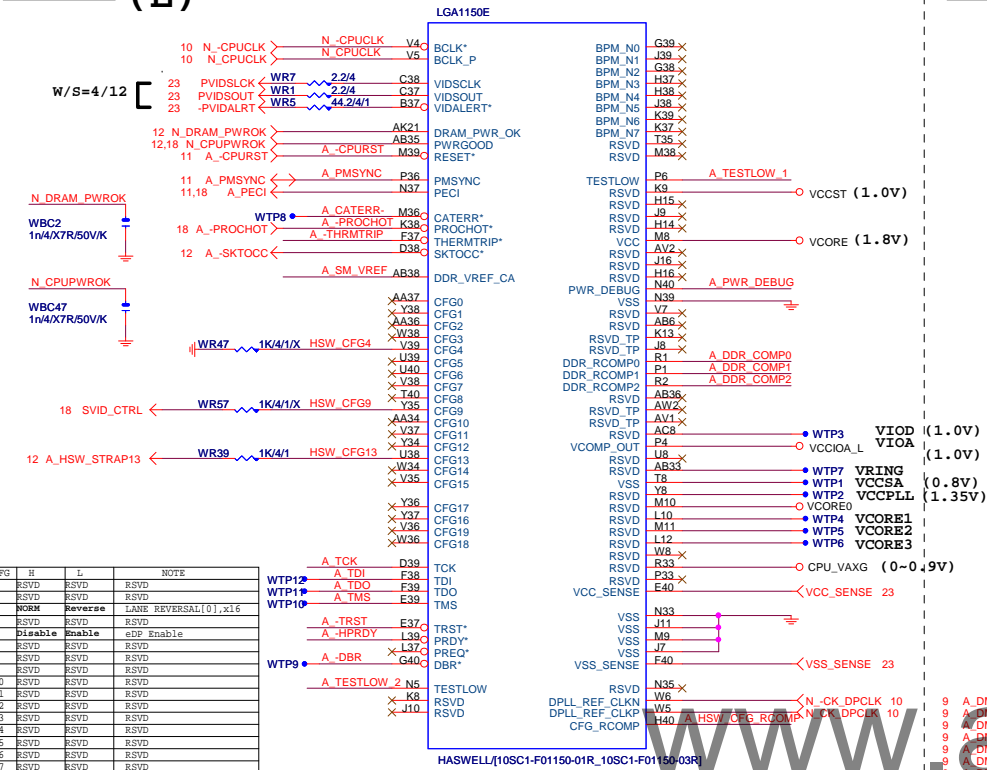
Data	Change Item	Reason
2012/12/11 EBOM:01	E-BOM	
2013/01/18 EBOM:02	1.修改HDMI阻值	
	2.修改cpu vcore load line /DVID 阻值	
	3.修改PROCHOT 阻值	
	4.修改DDR DIMM 顏色順序	
	5.DR166 1K--->5.76K FOR CPU loadline calibration	
2013/03/12 EBOM:10A	1.LEVEL SHIFT限用NXP	
	2.不用PROCHOT改用Thermtrip線路	
2013/03/22 PBOM:10B	1.NR177 ----> 0/4	
2013/03/29 PBOM:10C	1.DVI LEVEL SHIFT NXP----->ASM1442	
2013/05/13 PBOM:10D	1.change MOS-HS	
2013/06/28 PBOM:11A	1.Z87 C1 CHIP change TO C2	
	2.OR80 移除,dissable ITE OVP PROTECT	
2013/07/11 PBOM:11B	1.DEL CD1,R704,ADD R706,R705 715 ---->825	
2013/08/06 EBOM:01	1.Z87-DS3H E-BOM	
2013/09/02 EBOM:02	1.R676 改0603	
	2.DEL WR57,OR5,OR6,OR84	
2013/09/16 PBOM:11A	1.P-BOM	
2013/09/16 PBOM:11B	1.移除BC30,U10,R62 等,	
	1.變更UAR2,UAR3阻值	
2013/11/07 EBOM:01	Z97-DS3H-0.1 E-BOM	
2014/01/07 EBOM:01	H97-HD3-0.1 E-BOM	
2014/01/07 EBOM:02	1.add level shift IC ,Q3,R34	
2014/02/07 PBOM:10A	1.VR11,VR12,HR2,HR3 28K--->2.2K	
2014/03/05 EBOM:01	H97-DS3H-0.1 E-BOM	
2014/03/05 EBOM:10A	1.VCORE / NON VCORE MOS 統一用on mos	
	2.DBC27 10V ---->16V	
	H97 CHIP 的料號更換為20R FOR MP	

[illegible]

BLOCK DIAGRAM



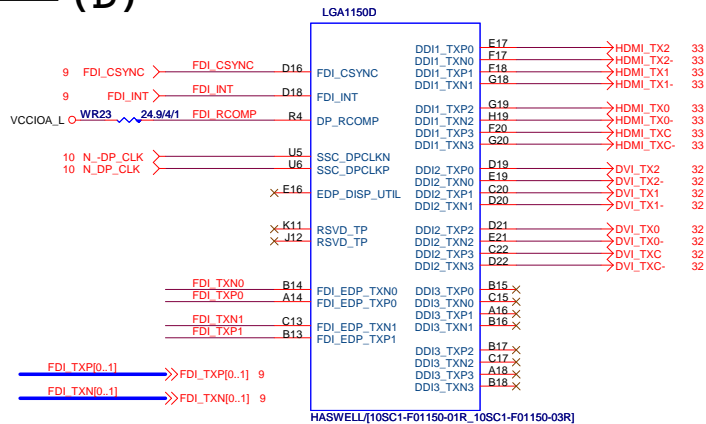
LGA1150 (E)



CFG6	CFG5	PCIE CONFIG
1	1	1x16 , Default
1	0	2x8
0	1	RSVD
0	0	X8, X4, X4

G 0-17 all internal PULL-UP

LGA1150 (D)



FDI:4/4/4//15(breakout min 4/4/4//8)
Impedance=85 +- 15%

DP/HDMI 4/4/4//20 FDI 4/4/4/12

Impedance=85 +- 15%

LGA1155 (C)

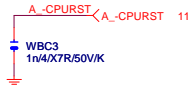


CPU PEG 5/5/5//20 Impedance=80 +- 15%

DMI 4/4/4//15 Impedance=85 +- 15%

-CPURST

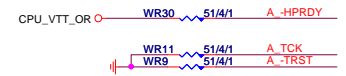
1.1V分壓



CPU SVID



CPU PU/PD

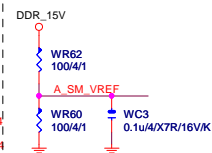


A_THRMTRIP WR70 1K/4/1 VCC1_05_PCH

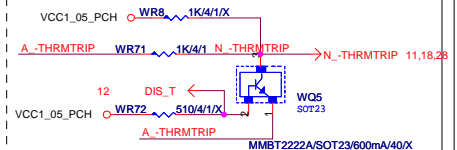
A PWR_DEBUG WR34 150/4/1 VCC1_05_PCH



SM REF



THRMTRIP DISABLE FOR Z87 OVERCLOCK



Gigabyte Technology

Title			
CPU LGA1150-A			
Size	Document Number		Rev
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LGA1150 (A)

LGA1150A									
		MAAA0	AU13	DDR0_MA0	DDR0_D00	AD38	MDA0		
		MAAA1	AV16	DDR0_MA1	DDR0_D01	AF39	MDA1		
		MAAA2	AU16	DDR0_MA2	DDR0_D02	AF38	MDA2		
		MAAA3	AW17	DDR0_MA3	DDR0_D03	AF39	MDA3		
		MAAA4	AU17	DDR0_MA4	DDR0_D04	AD37	MDA4		
		MAAA5	AW18	DDR0_MA5	DDR0_D05	AD40	MDA5		
		MAAA6	AV17	DDR0_MA6	DDR0_D06	AF37	MDA6		
		MAAA7	AU18	DDR0_MA7	DDR0_D07	AF40	MDA7		
		MAAA8	AV19	DDR0_MA8	DDR0_D08	AF39	MDA13		
		MAAA9	AU18	DDR0_MA9	DDR0_D09	AF40	MDA9		
		MAAA10	AW11	DDR0_MA10	DDR0_D10	AD38	MDA11		
		MAAA11	AU19	DDR0_MA11	DDR0_D11	AD39	MDA12		
		MAAA12	AY10	DDR0_MA12	DDR0_D12	AF38	MDA8		
		MAAA13	AY19	DDR0_MA13	DDR0_D13	AF37	MDA14		
		MAAA14	AT20	DDR0_MA14	DDR0_D14	AK40	MDA15		
		MAAA15	AW21	DDR0_MA15	DDR0_D15	MDA17			
				DDR0_D16	AM38	MDA21			
		MODT_A0	AW10	DDR0_ODT0	DDR0_D17	PM39	MDA18		
		MODT_A1	AY8	DDR0_ODT1	DDR0_D18	AP39	MDA19		
		MODT_A2	AW9	DDR0_ODT2	DDR0_D19	AM37	MDA20		
		MODT_A3	AU8	DDR0_ODT3	DDR0_D20	AM38	MDA16		
					DDR0_D21	AP37	MDA22		
					DDR0_D22	PM37	MDA25		
			AW33	DDR0_ECC0	DDR0_D23	AV35	MDA29		
			AU31	DDR0_ECC1	DDR0_D24	AW37	MDA29		
			AV31	DDR0_ECC2	DDR0_D25	AV35	MDA26		
			AU33	DDR0_ECC3	DDR0_D26	AV37	MDA27		
			AT33	DDR0_ECC4	DDR0_D27	AT35	MDA27		
			AT31	DDR0_ECC5	DDR0_D28	AV37	MDA28		
			AW31	DDR0_ECC6	DDR0_D29	AT35	MDA30		
				DDR0_ECC7	DDR0_D30	AW35	MDA31		
		SBAA0	AY12	DDR0_BA0	DDR0_D31	AY6	MDA33		
7		SBAA1	AT21	DDR0_BA1	DDR0_D32	AY6	MDA37		
7		SBAA2	AT21	DDR0_BA2	DDR0_D33	AY6	MDA37		
					DDR0_D34	AW4	MDA35		
					DDR0_D35	AW6	MDA36		
		CKEA0	CKEA0	DDR0_CK00	DDR0_D36	AW4	MDA32		
7		CKEA1	CKEA1	DDR0_CK01	DDR0_D37	AW4	MDA38		
7		CKEA2	CKEA2	DDR0_CK02	DDR0_D38	AW4	MDA39		
7		CKEA3	CKEA3	DDR0_CK03	DDR0_D39	AN1	MDA41		
					DDR0_D40	AN4	MDA42		
		-CSA0	-CSA1	DDR0_CS_N0	DDR0_D41	AN2	MDA48		
7		-CSA1	AY9	DDR0_CS_N1	DDR0_D42	AN4	MDA43		
7		-CSA2	AW10	DDR0_CS_N2	DDR0_D43	AN2	MDA44		
7		-CSA3	-CSA3	DDR0_CS_N3	DDR0_D44	AN2	MDA45		
					DDR0_D45	AN2	MDA46		
7		DCLKA0	DCLKA0	DDR0_CLK_P0	DDR0_D46	AN1	MDA47		
7		-DCLKA0	-DCLKA0	DDR0_CLK_N0	DDR0_D47	AL1	MDA49		
7		DCLKA1	DCLKA1	DDR0_CLK_P1	DDR0_D48	AL3	MDA50		
7		-DCLKA1	-DCLKA1	DDR0_CLK_N1	DDR0_D49	AL3	MDA50		
7		DCLKA2	DCLKA2	DDR0_CLK_P2	DDR0_D50	AL4	MDA51		
7		-DCLKA2	-DCLKA2	DDR0_CLK_N2	DDR0_D51	AL2	MDA52		
7		DCLKA3	DCLKA3	DDR0_CLK_P3	DDR0_D52	AL2	MDA54		
7		-DCLKA3	-DCLKA3	DDR0_CLK_N3	DDR0_D53	AL2	MDA54		
			AW12	RSVD	DDR0_D54	AL2	MDA55		
					DDR0_D55	AG1	MDA57		
					DDR0_D56	AG4	MDA61		
					DDR0_D57	AE3	MDA58		
					DDR0_D58	AE4	MDA59		
					DDR0_D59	AE2	MDA60		
					DDR0_D60	AE3	MDA56		
					DDR0_D61	AE3	MDA62		
					DDR0_D62	AE1	MDA63		
7		-SRASA	-SRASA	DDR0_RAS*	DDR0_DOS_P0	AE39	DOSA0		
7		-SWEA	-SWEA	DDR0_WE*	DDR0_DOS_P1	AN39	DOSA2		
					DDR0_DOS_P2	AV36	DOSA3		
			AW20	RSVD	DDR0_DOS_P3	AE4	DOSA4		
			AW27C	RSVD	DDR0_DOS_P4	AP3	DOSA5		
					DDR0_DOS_P5	AP3	DOSA6		
7		-SCASA	-SCASA	DDR0_CAS*	DDR0_DOS_P6	AE3	DOSA7		
					DDR0_DOS_P7	AE2			
7.8		-DDR3_RST	WR61 D4/SH/TMX	AKK22	DDR0_DESCRIPTOR*	AV32	DOSA0		
			WC4			AE38	DOSA1		
			0.1uA/XCTR/16V/KX			AN38	DOSA2		
						AN36	DOSA3		
						AW5	DOSA4		
						AE2	DOSA5		
						AE2	DOSA6		
						AE2	DOSA7		
						AKK22			

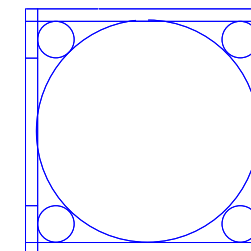
HASWELL/10SC1-F01150-01R 10SC1-F01150-03R]

LGA1150 (B)

LGA1150B					
MAA80	AL19	DDR1_MA0	DDR1_DQ0	AE34	MD80
MAA81	AK23	DDR1_MA1	DDR1_DQ1	AE35	MD81
MAA82	AM22	DDR1_MA2	DDR1_DQ2	AE36	MD82
MAA83	AM23	DDR1_MA3	DDR1_DQ3	AH35	MD83
MAA84	AF23	DDR1_MA4	DDR1_DQ4	AD34	MD84
MAA85	AL23	DDR1_MA5	DDR1_DQ5	AD35	MD85
MAA86	AY24	DDR1_MA6	DDR1_DQ6	AG34	MD86
MAA87	AV25	DDR1_MA7	DDR1_DQ7	AH34	MD87
MAA88	AL26	DDR1_MA8	DDR1_DQ8	AH35	MD88
MAA89	AP25	DDR1_MA9	DDR1_DQ9	AK35	MD89
MAA90	AW18	DDR1_MA10	DDR1_DQ10	AK31	MD810
MAA91	AY25	DDR1_MA11	DDR1_DQ11	AL31	MD811
MAA92	AV26	DDR1_MA12	DDR1_DQ12	AL32	MD812
MAA93	AL15	DDR1_MA13	DDR1_DQ13	AK32	MD813
MAA94	AY27	DDR1_MA14	DDR1_DQ14	AL32	MD814
MAA95	AV28	DDR1_MA15	DDR1_DQ15	AK31	MD815
			DDR1_DQ16	AK34	MD821
MODT_B0	AM17	DDR1_ODT0	DDR1_DQ17	AF34	MD819
MODT_B1	AL16	DDR1_ODT1	DDR1_DQ18	AP31	MD823
MODT_B2	AM16	DDR1_ODT2	DDR1_DQ19	AF35	MD820
MODT_B3	AK15	DDR1_ODT3	DDR1_DQ20	AK35	MD816
			DDR1_DQ21	AK32	MD818
	AM26	DDR1_EC00	DDR1_DQ22	AP32	MD822
	AM25	DDR1_EC01	DDR1_DQ23	AM29	MD825
	AF25	DDR1_EC02	DDR1_DQ24	AM29	MD828
	AF26	DDR1_EC03	DDR1_DQ25	AM28	MD827
	AR26	DDR1_EC04	DDR1_DQ26	AR28	MD830
	AR25	DDR1_EC05	DDR1_DQ27	AL28	MD829
	AR26	DDR1_EC06	DDR1_DQ28	AP29	MD826
	AR25	DDR1_EC07	DDR1_DQ29	AP28	MD831
			DDR1_DQ30	AP12	MD832
SBA80	AK17	DDR1_BA0	DDR1_DQ31	AL13	MD834
SBA81	AW18	DDR1_BA1	DDR1_DQ32	AL12	MD835
SBA82	AL28	DDR1_BA2	DDR1_DQ33	AL13	MD836
			DDR1_DQ34	AL12	MD838
CKE80	AW29	DDR1_CKE0	DDR1_DQ35	AM13	MD838
CKE81	AY29	DDR1_CKE1	DDR1_DQ36	AM12	MD838
CKE82	AL29	DDR1_CKE2	DDR1_DQ37	AR9	MD845
CKE83	AL29	DDR1_CKE3	DDR1_DQ38	AR6	MD847
			DDR1_DQ39	AP6	MD844
CS80	AP17	DDR1_CS_N0	DDR1_DQ40	AR10	MD843
CS81	AN15	DDR1_CS_N1	DDR1_DQ41	AR7	MD846
CS82	AN17	DDR1_CS_N2	DDR1_DQ42	AP7	MD842
CS83	AL15	DDR1_CS_N3	DDR1_DQ43	AR9	MD852
			DDR1_DQ44	AM6	MD850
			DDR1_DQ45	AM10	MD848
			DDR1_DQ46	AM10	MD848
DCLK80	AM20	DDR1_CLK_P0	DDR1_DQ47	AM7	MD854
DCLK81	AM21	DDR1_CLK_P1	DDR1_DQ48	AM7	MD851
DCLK82	AN21	DDR1_CLK_P2	DDR1_DQ49	AH6	MD861
DCLK83	AP19	DDR1_CLK_N2	DDR1_DQ50	AH7	MD859
DCLK84	AP20	DDR1_CLK_N3	DDR1_DQ51	A66	MD863
SCASB	AP16C	DDR1_CAS*	DDR1_DQ52	A67	MD856
SRASB	AM18C	DDR1_RAS*	DDR1_DQ53	AF7	MD857
SWEB	AK16C	DDR1_WE*	DDR1_DQ54	AF7	MD862
			DDR1_DQ55	AF35	MD850
	AB39	DDR_VREF_DQ0	DDR1_DQ56	AL33	CS881
	AB40	DDR_VREF_DQ1	DDR1_DQ57	AP33	CS882
			DDR1_DQ58	AN28	CS883
			DDR1_DQ59	AN12	CS884
			DDR1_DQ60	AP8	CS885
			DDR1_DQ61	AL8	CS886
			DDR1_DQ62	AG7	CS887
			DDR1_DQ63	AN2X	CS888
			DDR1_DQ64	AF34	CS880
			DDR1_DQ65	AK33	CS881
			DDR1_DQ66	AN33	CS882
			DDR1_DQ67	AN29	CS883
			DDR1_DQ68	AN13	CS884
			DDR1_DQ69	AL8	CS885
			DDR1_DQ70	AG6	CS886
			DDR1_DQ71	AG6	CS887
			DDR1_DQ72	AN2X	CS888

HASWELL/10SC1-F01150-01R 10SC1-F01150-03R

LGA1150 (CR)

LGA1150
ILM_BP_CR/115X/NORMAL NI

DDR BUS

7 MODT_A[0..3] ↔ MODT_A[0..3]
 8 MODT_B[0..3] ↔ MODT_B[0..3]
 7 MDA[0..63] ↔ MDA[0..63]
 8 MDB[0..63] ↔ MDB[0..63]
 7 DQSA[0..7] ↔ DQSA[0..7]
 8 -DQSA[0..7] ↔ -DQSA[0..7]
 7 MAAA[0..15] ↔ MAAA[0..15]
 8 MAAB[0..15] ↔ MAAB[0..15]
 8 DQSB[0..7] ↔ DQSB[0..7]
 8 -DQSB[0..7] ↔ -DQSB[0..7]

(F, J)



(G,H,I)



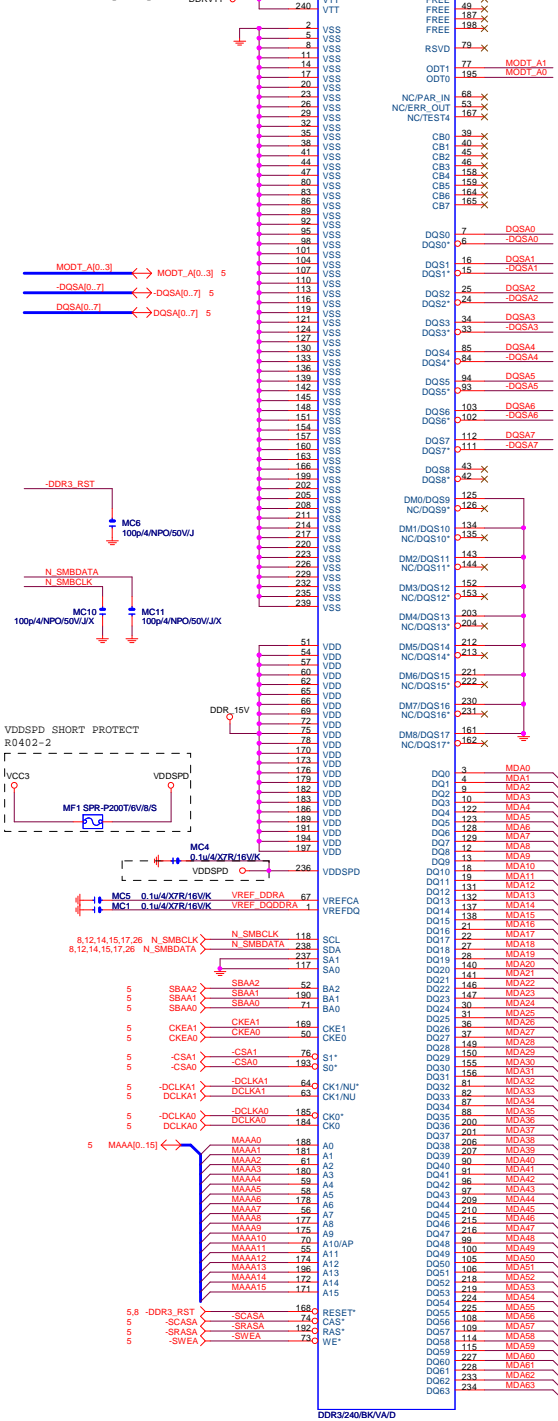
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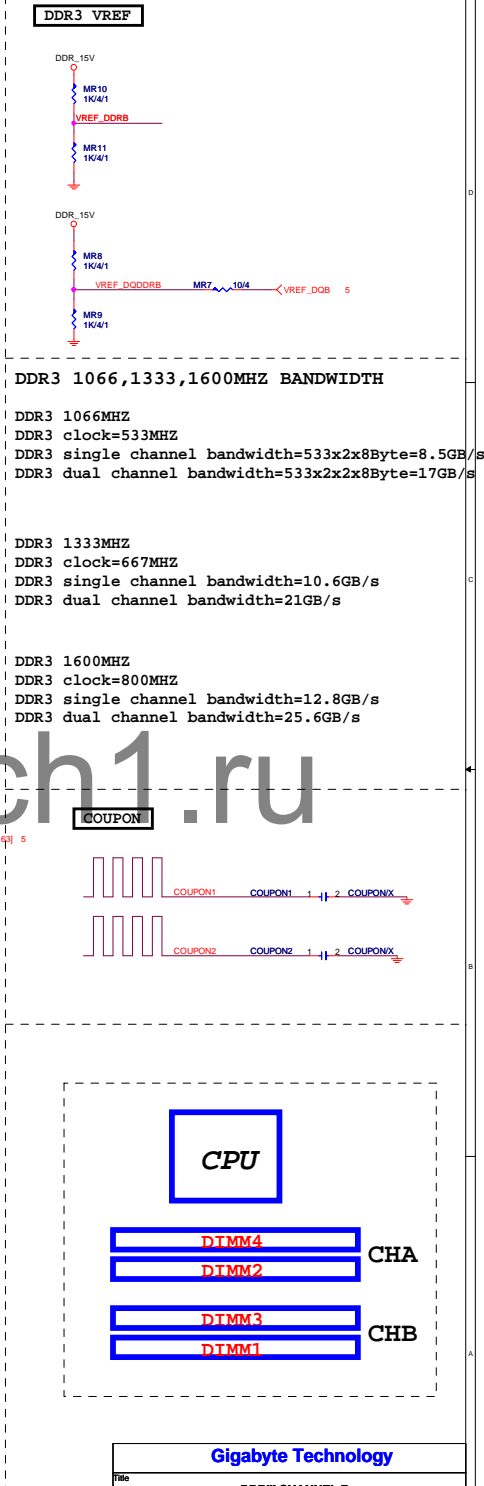
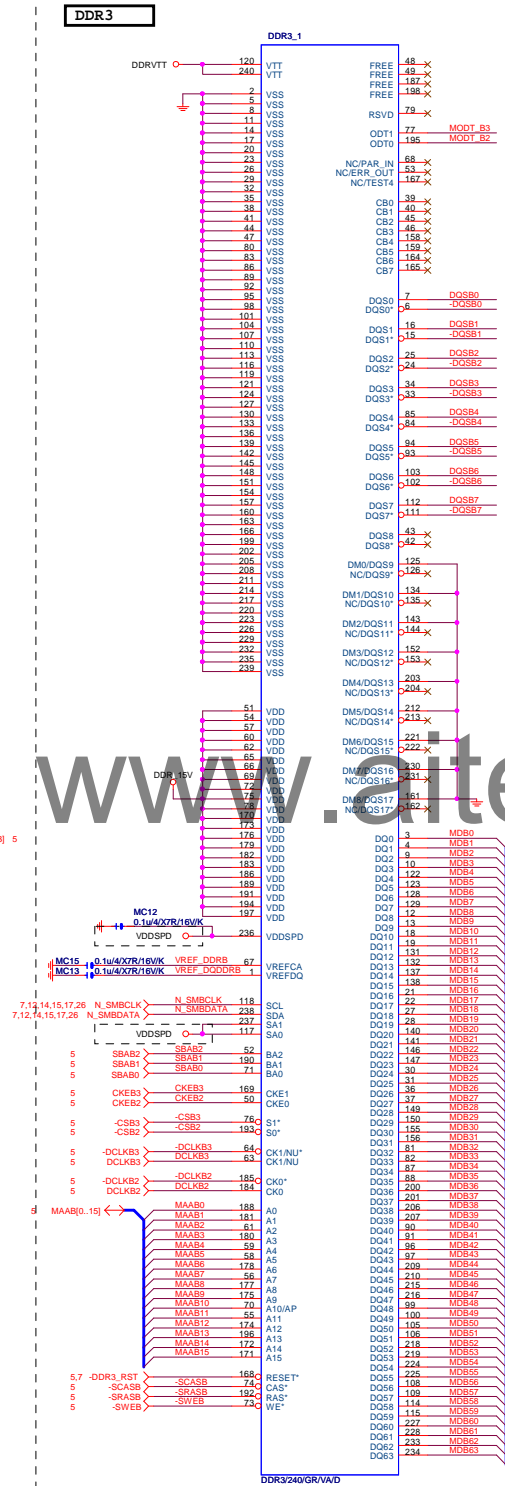
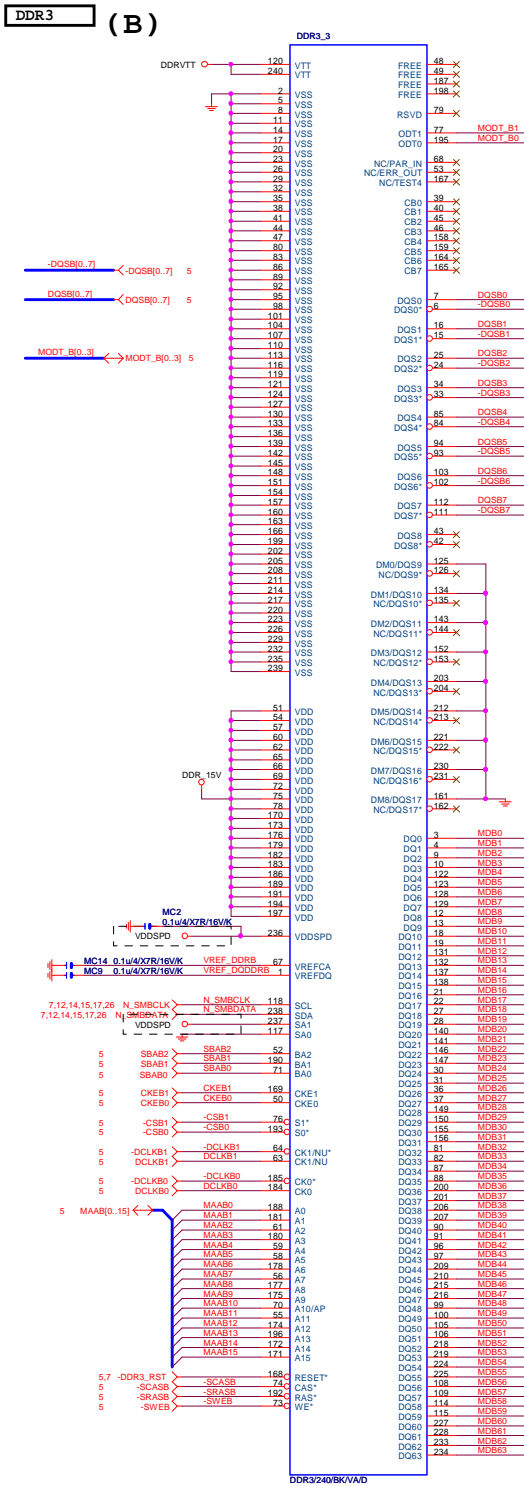


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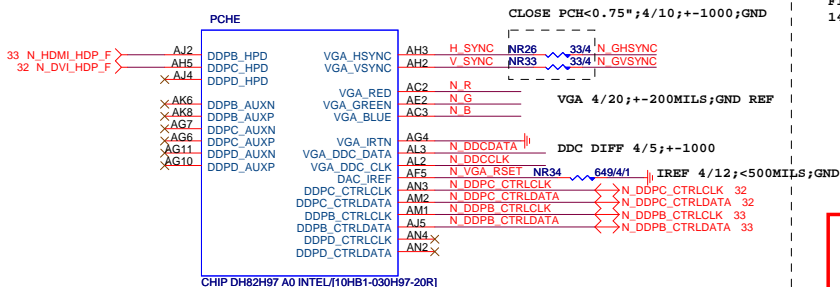


DDR3 (A)



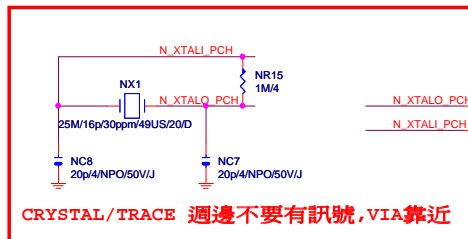
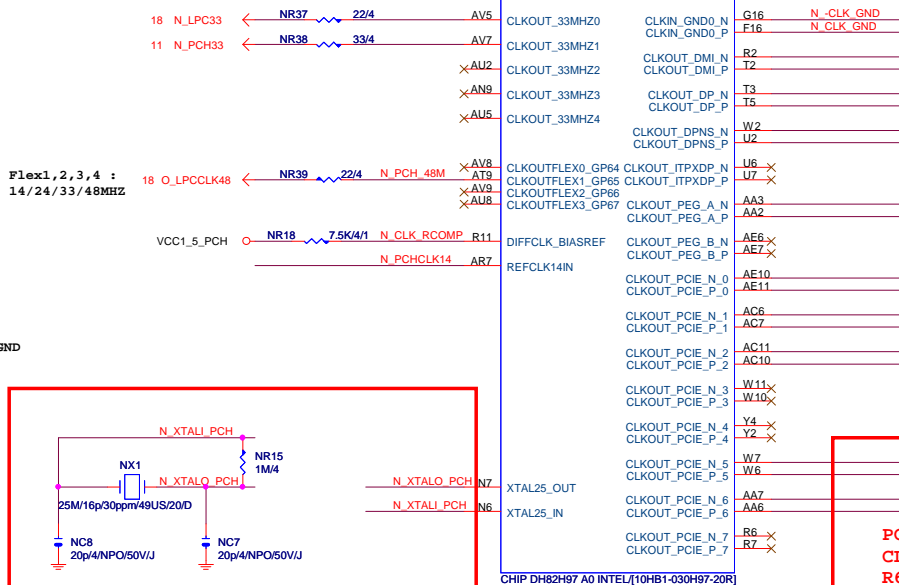


PCH (E)



VGA DISABLE	
R,G,B	NC OR GND
IRTN / IREF	GND
VGA_HSYNC, VGA_VSYNC, DDC_CLK, DDC_DATA	NC
POWER VCCADAC(AF2), VCCADACBG(AE1)	GND

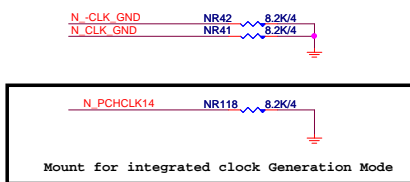
PCH (G)



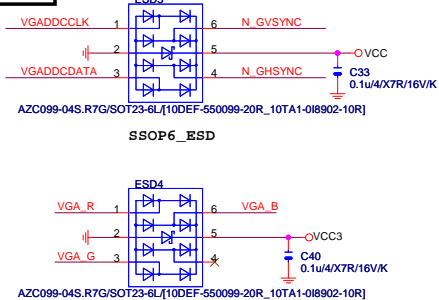
PCIEX4
CLOCK(PE_SRCCLK_3GIO1)由PIN
R6,R7 換成PIN W7,W6
避免跟CRYSTAL 25MHZ干擾

Differential Clock:18/4/6/4/18
Impedance=90 +- 15%

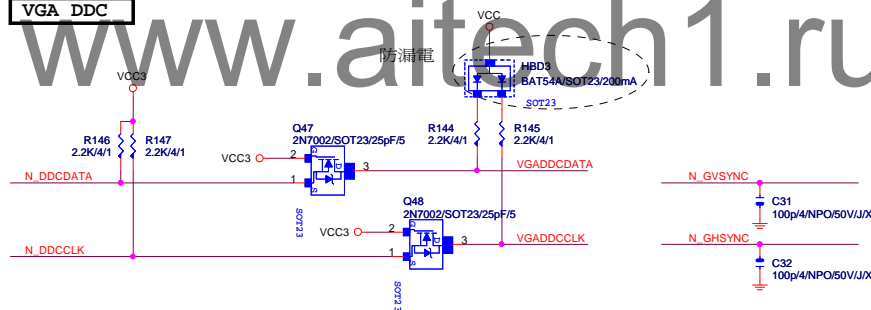
PCH CLK PD



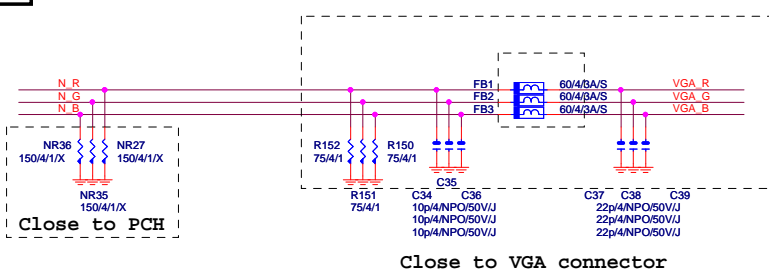
VGA ESD



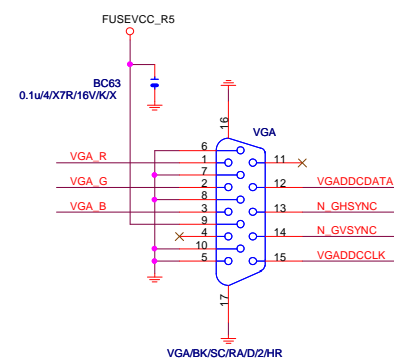
VGA DDC



VGA DDC



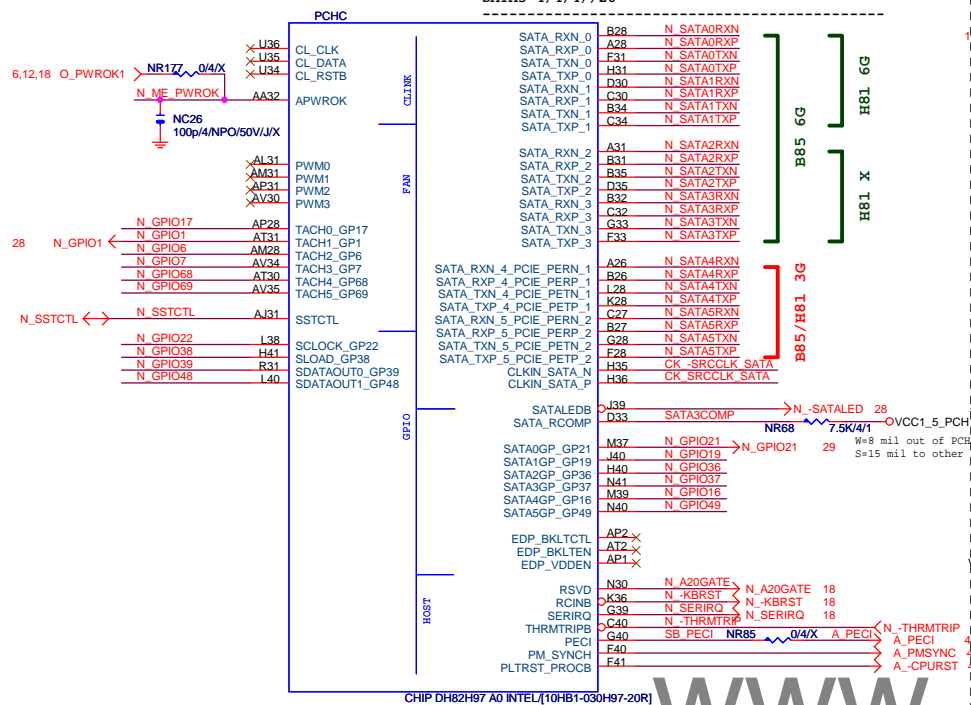
VGA CONNECTOR



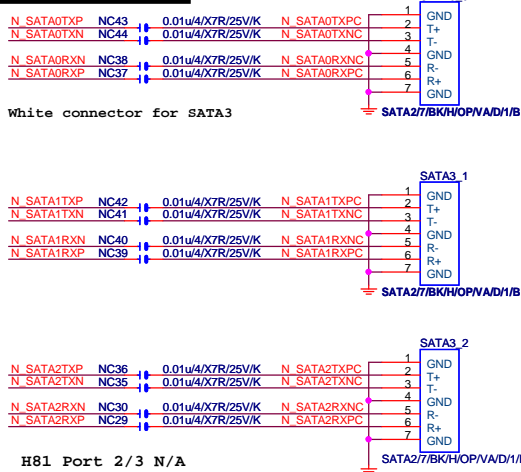
Gigabyte Technology

Title				
PCH DISPLAY ,CLK BUFFER				
Size	Document Number			Rev
Custom	GA-H97-DS3H			1.0
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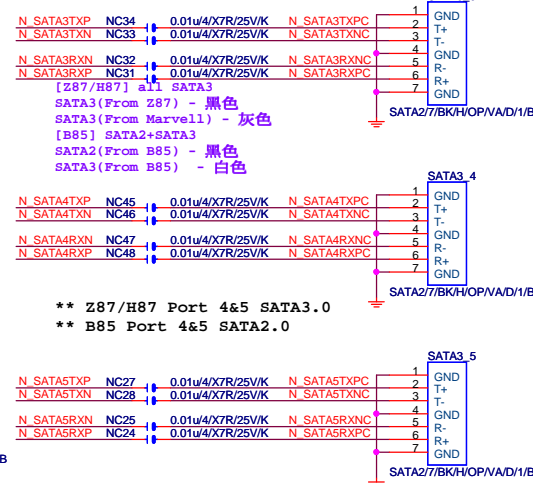
PCH (C)



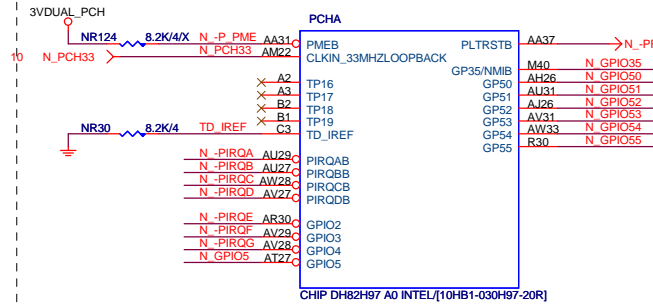
SATA CONNECTOR



```
** Z87/H87 Port 4&5 SATA3.0
** B85 Port 4&5 SATA2.0
```



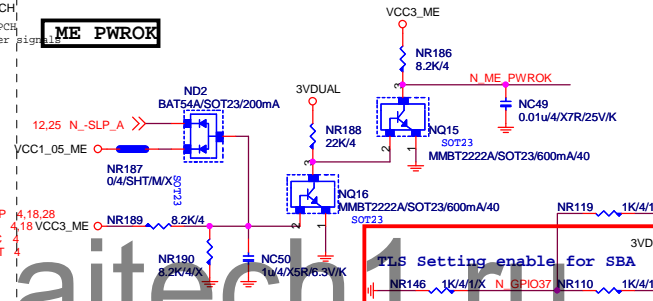
PCH (A)



```
Default int pull up on GP51,
Default SPI boot devices
```

BOOT DEVICE	GP51	GP19
LPC	0	0
SPI	float	float

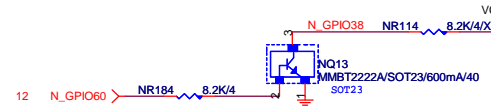
ME PWROK



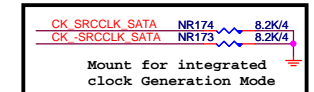
GPIO38 Ctrl

MFG Mode

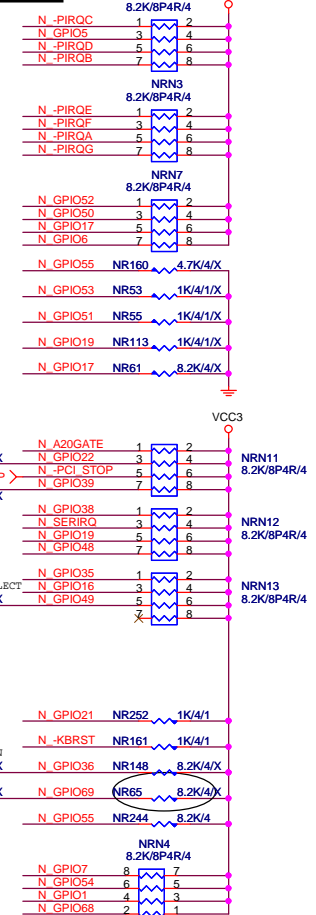
```
N_GPIO38 : Lo --> Enable
           Hi --> Disable
```



PCH CLK PD



PCH	PU/PD
-----	-------

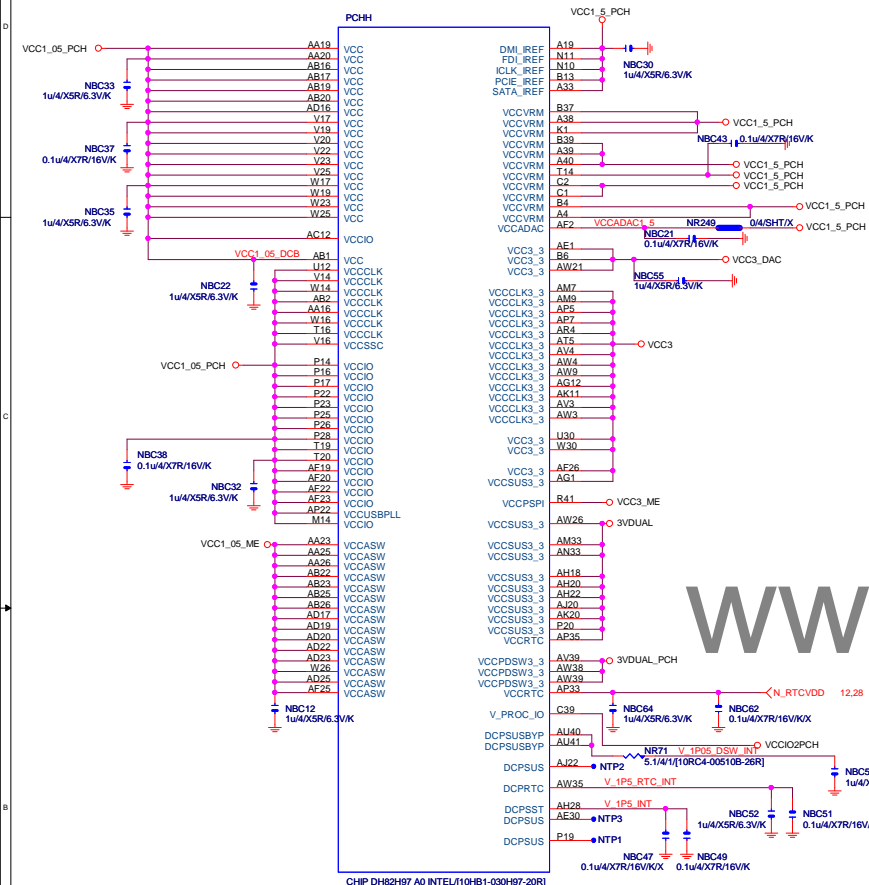


soft strap	GP16	GP49
0	pcie1	pcie2
1	sata4	sata5

Gigabyte Technology

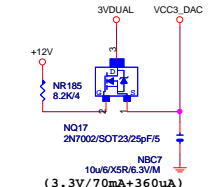
Title			
PCH HOST , SATA, PCI			
Size	Document Number		Rev
Custom	GA-H97-DS3H		1.0
Date:	Tuesday, April 15, 2014	Sheet	11 of 34

PCH (H)

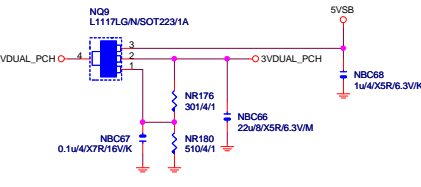


VCC3_DAC

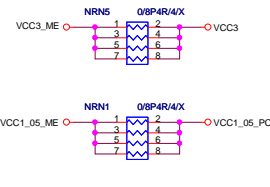
CLOSE北橋(注意震盪水波紋)



3VDUAL_PCH

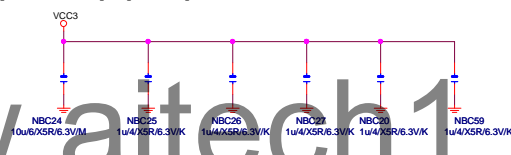


SHT_PWR

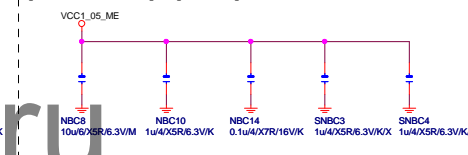


CAP

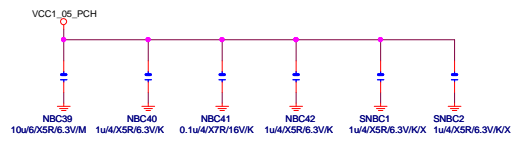
(3.3V) (X6)



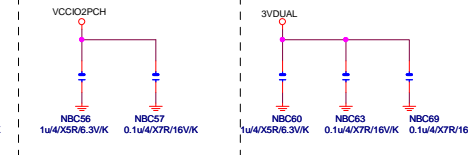
(1.05V) (X5)



(1.05V) (X6)

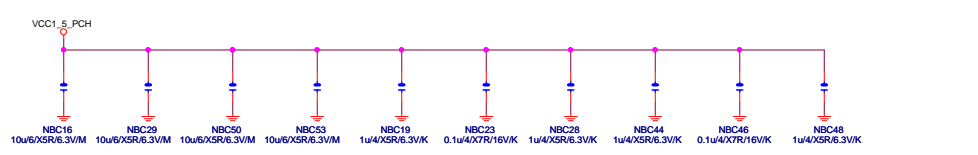


(1.05V) (X2)



(3.3V) (X2)

(1.5V) (X10)

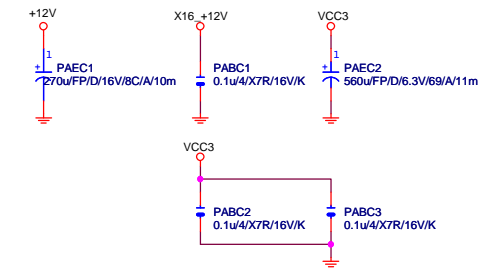


PCH (I)



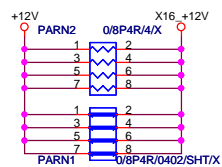
PCH
CHP DH82H97 A0 INTEL[10HB1-030H97-20R]

PCIEX16 CAP



PCIEX16 PROTECT SHT

+12 protect short-wire test



PCIEX16 AC CAP

PA EXP TXP0	PAC5	0.22u/4/X5R/6.3V/K	PA EXP TXP0 C
PA EXP TXN0	PAC4	0.22u/4/X5R/6.3V/K	PA EXP TXN0 C
PA EXP TXP1	PAC6	0.22u/4/X5R/6.3V/K	PA EXP TXP1 C
PA EXP TXN1	PAC7	0.22u/4/X5R/6.3V/K	PA EXP TXN1 C
PA EXP TXP2	PAC8	0.22u/4/X5R/6.3V/K	PA EXP TXP2 C
PA EXP TXN2	PAC9	0.22u/4/X5R/6.3V/K	PA EXP TXN2 C
PA EXP TXP3	PAC10	0.22u/4/X5R/6.3V/K	PA EXP TXP3 C
PA EXP TXN3	PAC11	0.22u/4/X5R/6.3V/K	PA EXP TXN3 C
PA EXP TXP4	PAC12	0.22u/4/X5R/6.3V/K	PA EXP TXP4 C
PA EXP TXN4	PAC13	0.22u/4/X5R/6.3V/K	PA EXP TXN4 C
PA EXP TXP5	PAC14	0.22u/4/X5R/6.3V/K	PA EXP TXP5 C
PA EXP TXN5	PAC15	0.22u/4/X5R/6.3V/K	PA EXP TXN5 C
PA EXP TXP6	PAC16	0.22u/4/X5R/6.3V/K	PA EXP TXP6 C
PA EXP TXN6	PAC17	0.22u/4/X5R/6.3V/K	PA EXP TXN6 C
PA EXP TXP7	PAC19	0.22u/4/X5R/6.3V/K	PA EXP TXP7 C
PA EXP TXN7	PAC18	0.22u/4/X5R/6.3V/K	PA EXP TXN7 C
PA EXP TXP8	PAC20	0.22u/4/X5R/6.3V/K	PA EXP TXP8 C
PA EXP TXN8	PAC21	0.22u/4/X5R/6.3V/K	PA EXP TXN8 C
PA EXP TXP9	PAC22	0.22u/4/X5R/6.3V/K	PA EXP TXP9 C
PA EXP TXN9	PAC23	0.22u/4/X5R/6.3V/K	PA EXP TXN9 C
PA EXP TXP10	PAC24	0.22u/4/X5R/6.3V/K	PA EXP TXP10 C
PA EXP TXN10	PAC25	0.22u/4/X5R/6.3V/K	PA EXP TXN10 C
PA EXP TXP11	PAC26	0.22u/4/X5R/6.3V/K	PA EXP TXP11 C
PA EXP TXN11	PAC27	0.22u/4/X5R/6.3V/K	PA EXP TXN11 C
PA EXP TXP12	PAC28	0.22u/4/X5R/6.3V/K	PA EXP TXP12 C
PA EXP TXN12	PAC29	0.22u/4/X5R/6.3V/K	PA EXP TXN12 C
PA EXP TXP13	PAC30	0.22u/4/X5R/6.3V/K	PA EXP TXP13 C
PA EXP TXN13	PAC31	0.22u/4/X5R/6.3V/K	PA EXP TXN13 C
PA EXP TXP14	PAC32	0.22u/4/X5R/6.3V/K	PA EXP TXP14 C
PA EXP TXN14	PAC33	0.22u/4/X5R/6.3V/K	PA EXP TXN14 C
PA EXP TXP15	PAC34	0.22u/4/X5R/6.3V/K	PA EXP TXP15 C
PA EXP TXN15	PAC35	0.22u/4/X5R/6.3V/K	PA EXP TXN15 C

PCI-E REV:1.1--> 2.5GHZ

PCE-E X1(單向) BANDWITH=2.5GHz*(8b/10b)=2Gb/s=250MB/s

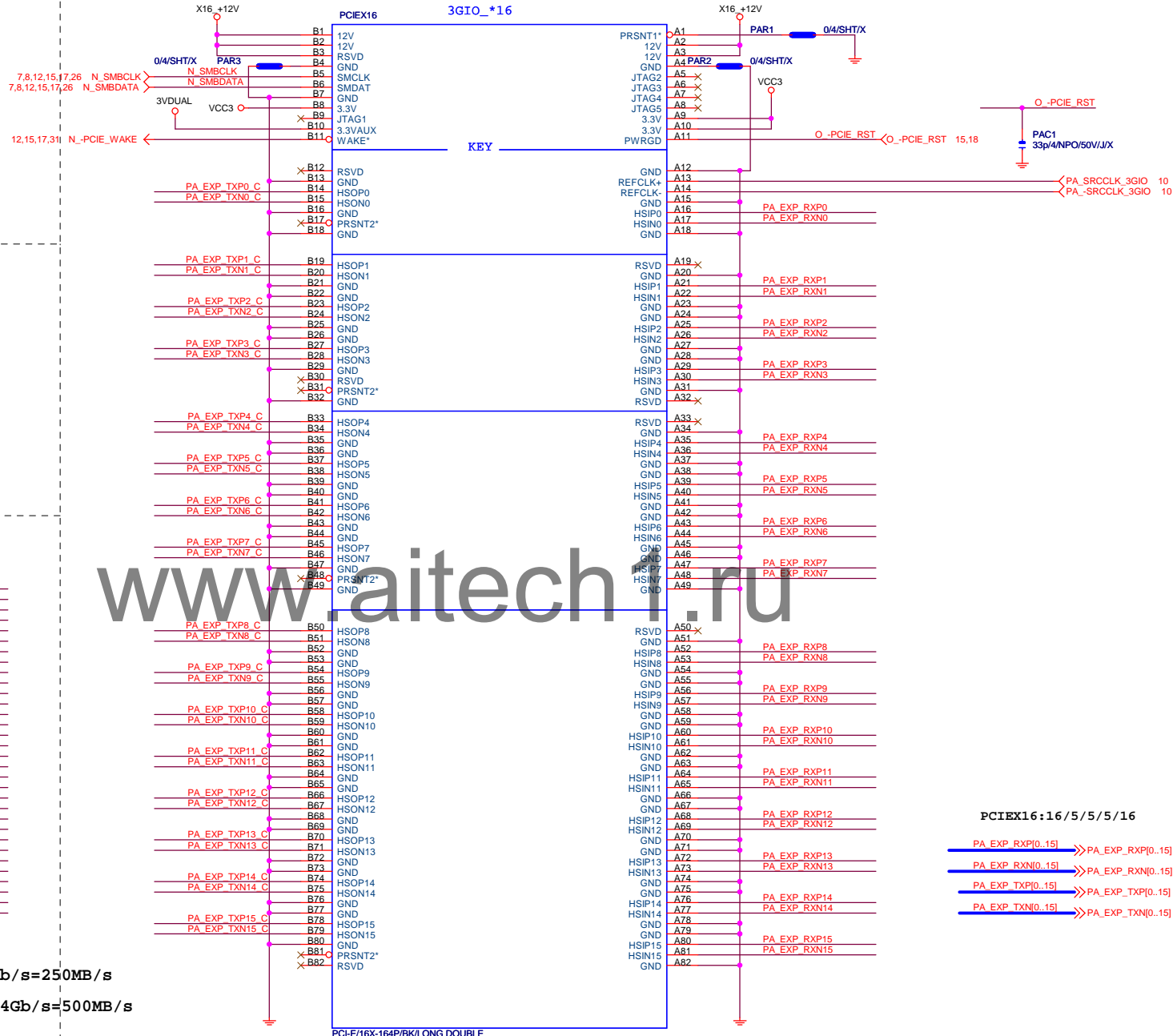
PCE-E X1(雙向) BANDWITH=2.5GHz*(8b/10b)X2=4Gb/s=500MB/s

PCE-E X16(單向) BANDWITH=2.5GHz*(8b/10b)X16=32Gb/s=4GB/s

PCE-E X16(雙向) BANDWITH=2.5GHz*(8b/10b)X16X2=64Gb/s=8GB/s

PCI-E REV:2.0--> 5GHZ

PCIEX16 SLOT

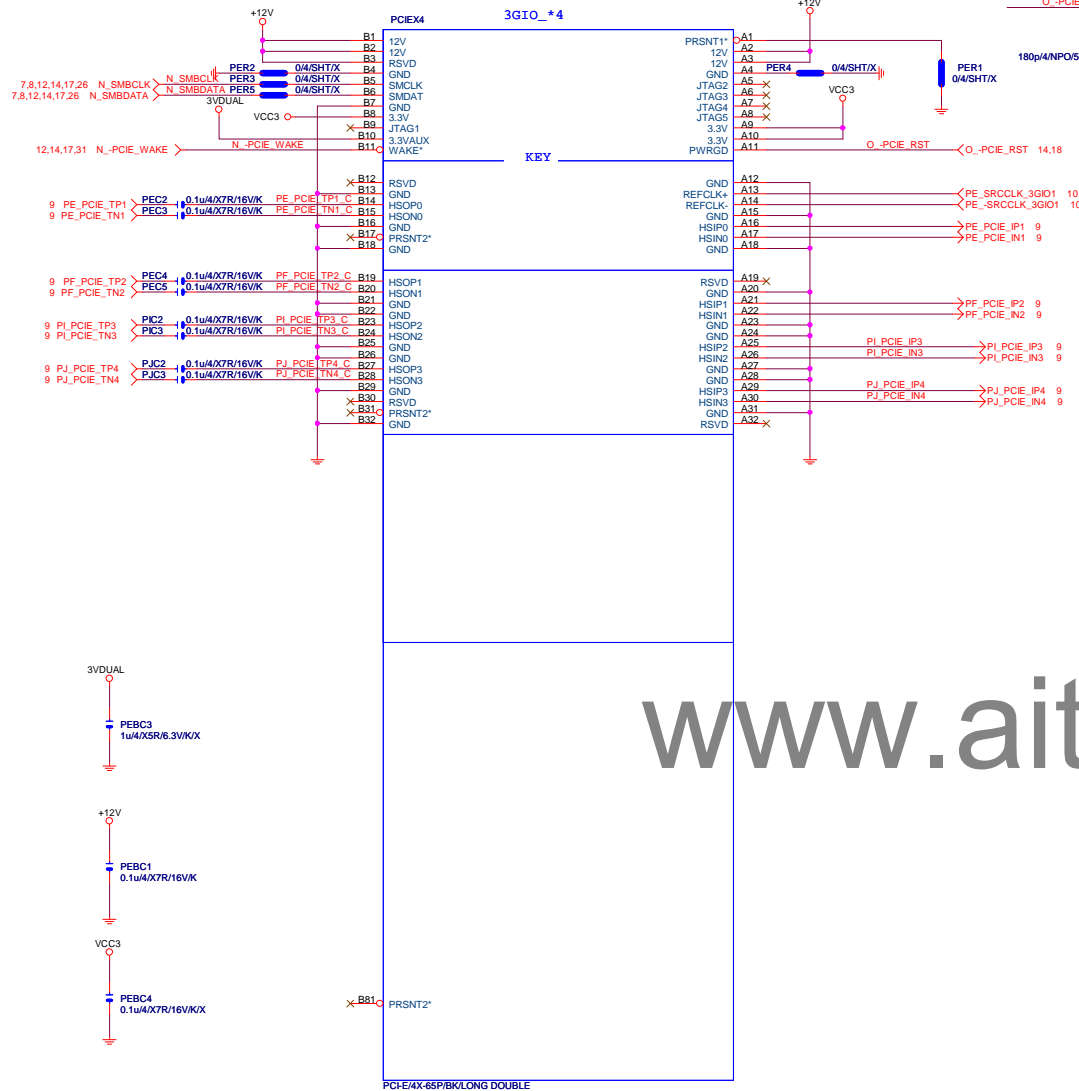


PCIEX16:16/5/5/5/16

PA EXP RXP0..15]	>>>PA_EXP_RXP[0..15]	4
PA EXP RXN0..15]	>>>PA_EXP_RXN[0..15]	4
PA EXP TXP[0..15]	>>>PA_EXP_TXP[0..15]	4
PA EXP TXN[0..15]	>>>PA_EXP_TXN[0..15]	4

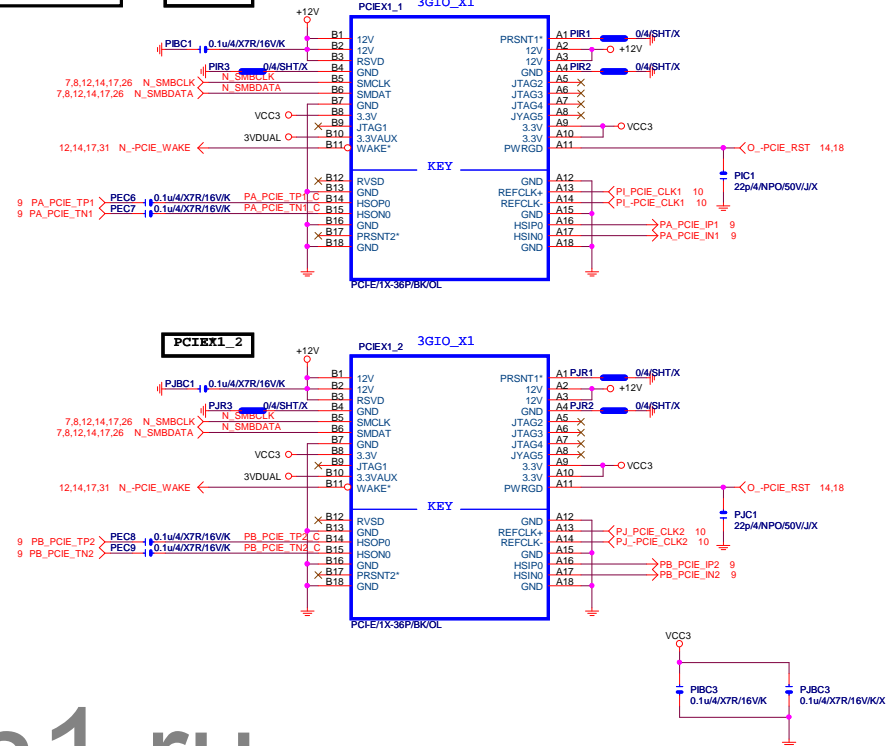
Gigabyte Technology			
PCI EXPRESS * 16			
Title	Document Number	GA-H97-HD3	Rev 0.2
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PCIEX4 SLOT



PCIEX1 SLOT

PCIEX1_1

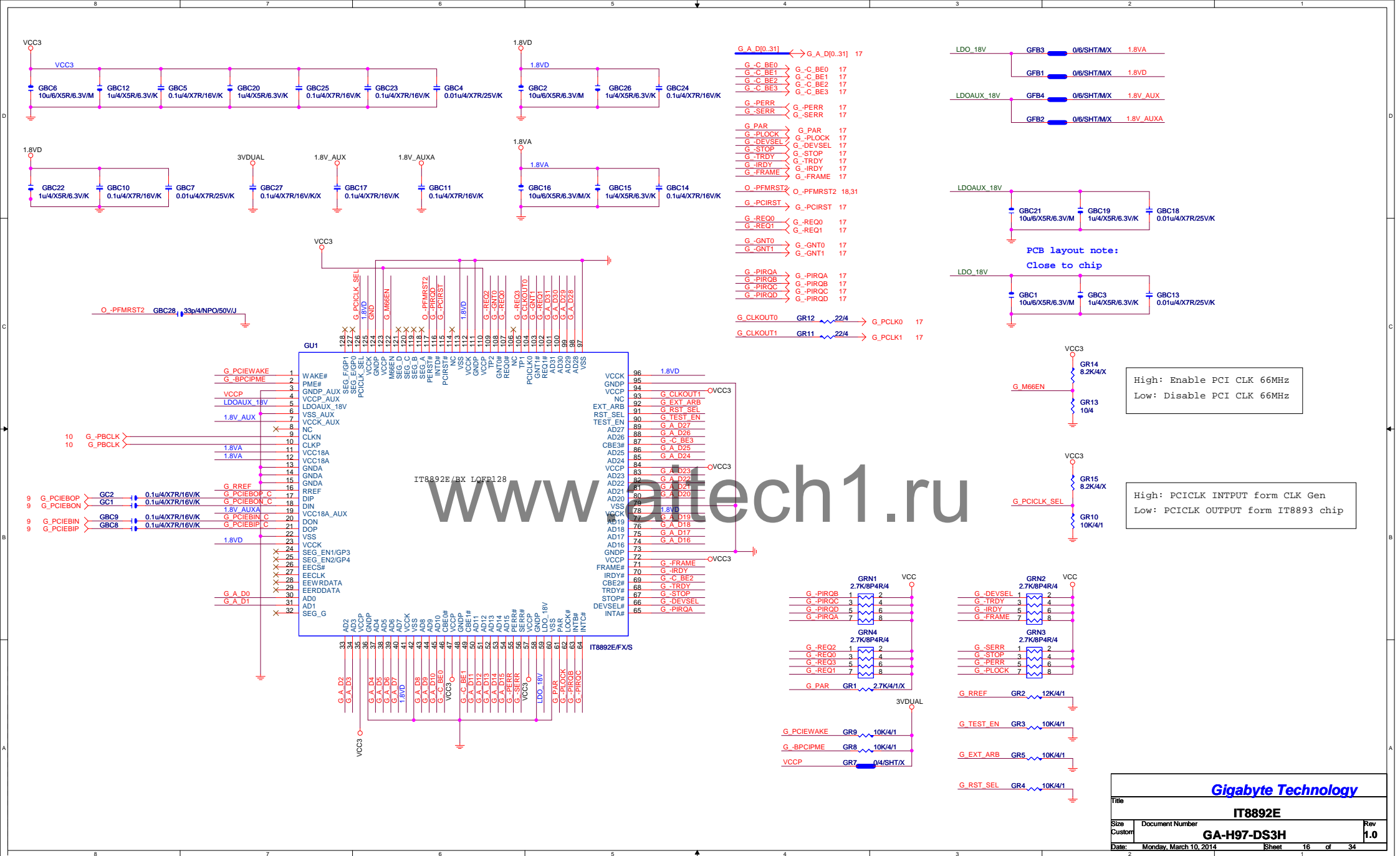


PCIEX4/X1 SWITCH

	N_PCIE_4_SW (PCH GPIO48)	PCIEX4_X1 (SIO_GPIO26)
PCIEX4 No devices	H	H
PCIEX4 -> X1	H	H
PCIEX4 Have devices	L	L
PCIEX4 -> X4	L	L
PCIEX1_1/2 --> N/A		

Function	SEL
xI--> xOa	L;PCIEX4 SLOT-->X1
xI--> xOb	H;PCIEX4 SLOT-->X4

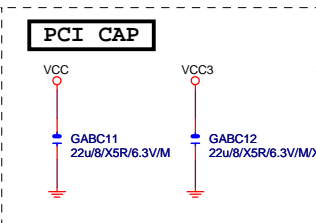
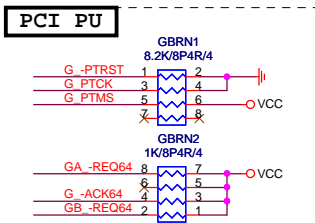
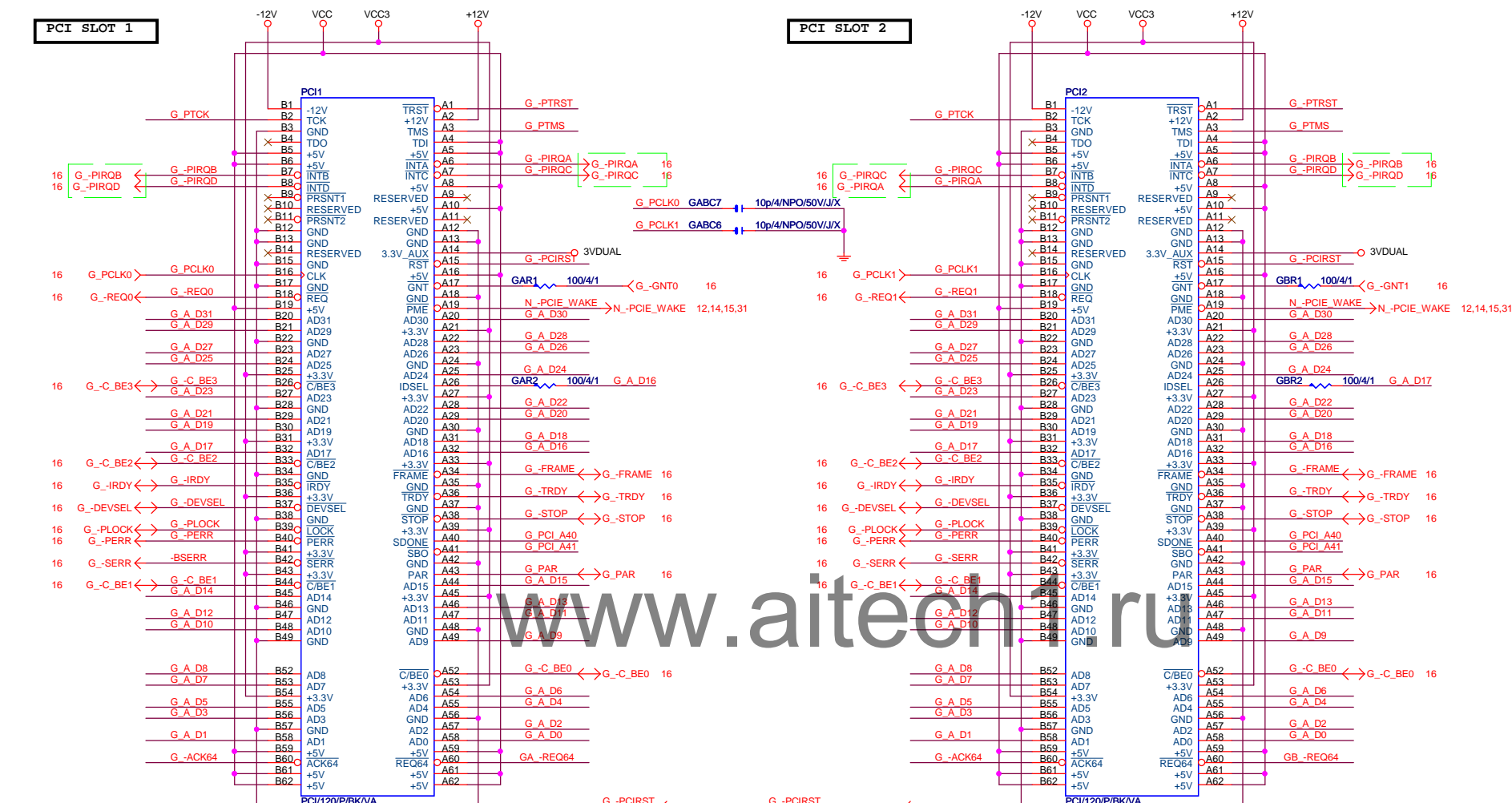
Gigabyte Technology			
Title	PCIEX1 X1 1,2,3		
Size	Document Number	Rev	
Custom	GA-H97-DS3H	1.0	
Date:	Monday, March 10, 2014	Sheet	15 of 34



Gigabyte Technology		
Title		
IT8892E		
Size	Document Number	Rev
Custom	GA-H97-DS3H	1.0
Date:	Monday, March 10, 2014	Sheet 16 of 34

PCI SLOT 1

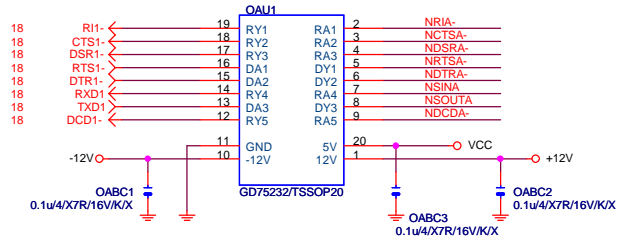
PCI SLOT 2



PCI SLOT 1&2

Size	Document Number	Rev
Custom	GA-H97-DS3H	1.0
Date:	Monday, March 10, 2014	Sheet 17 of 34

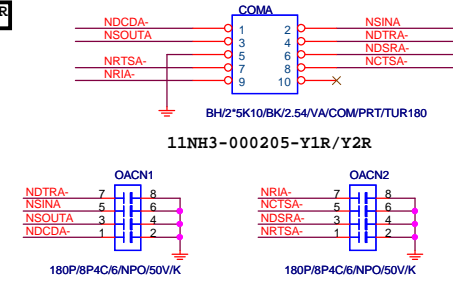
COMA



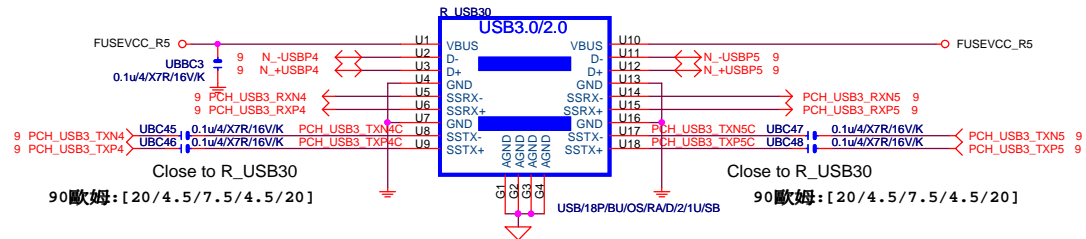
COM RI



COM BUFFER

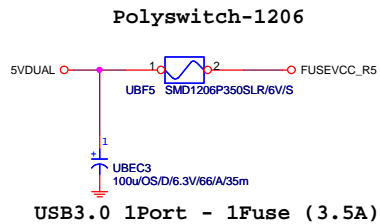


USB30_20 CONNECT

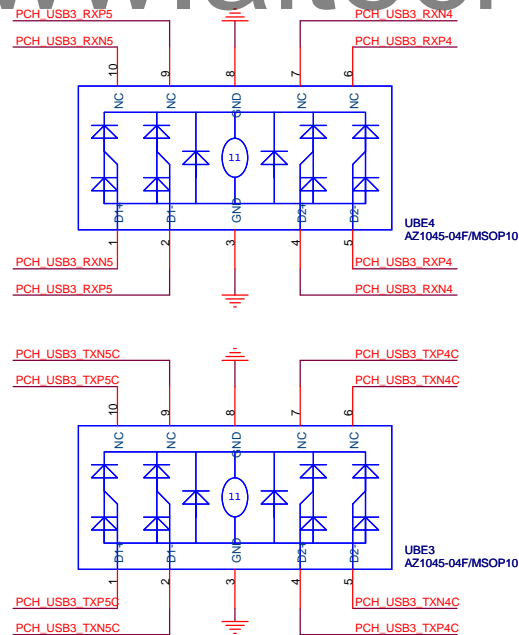


-PROHOT

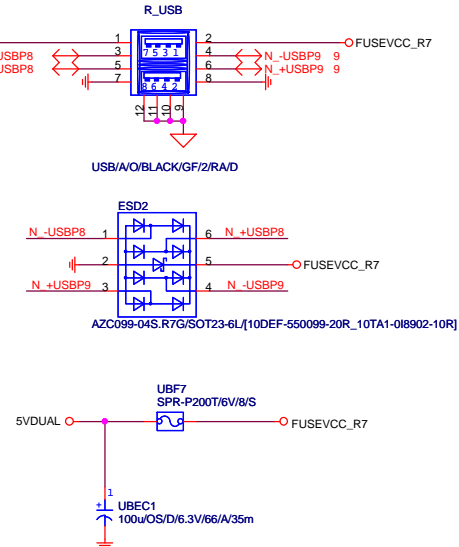
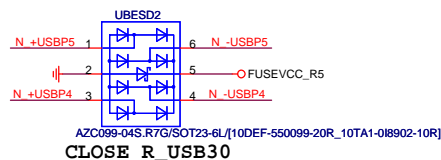
USB30_PWR



USB30 ESD PROTECT



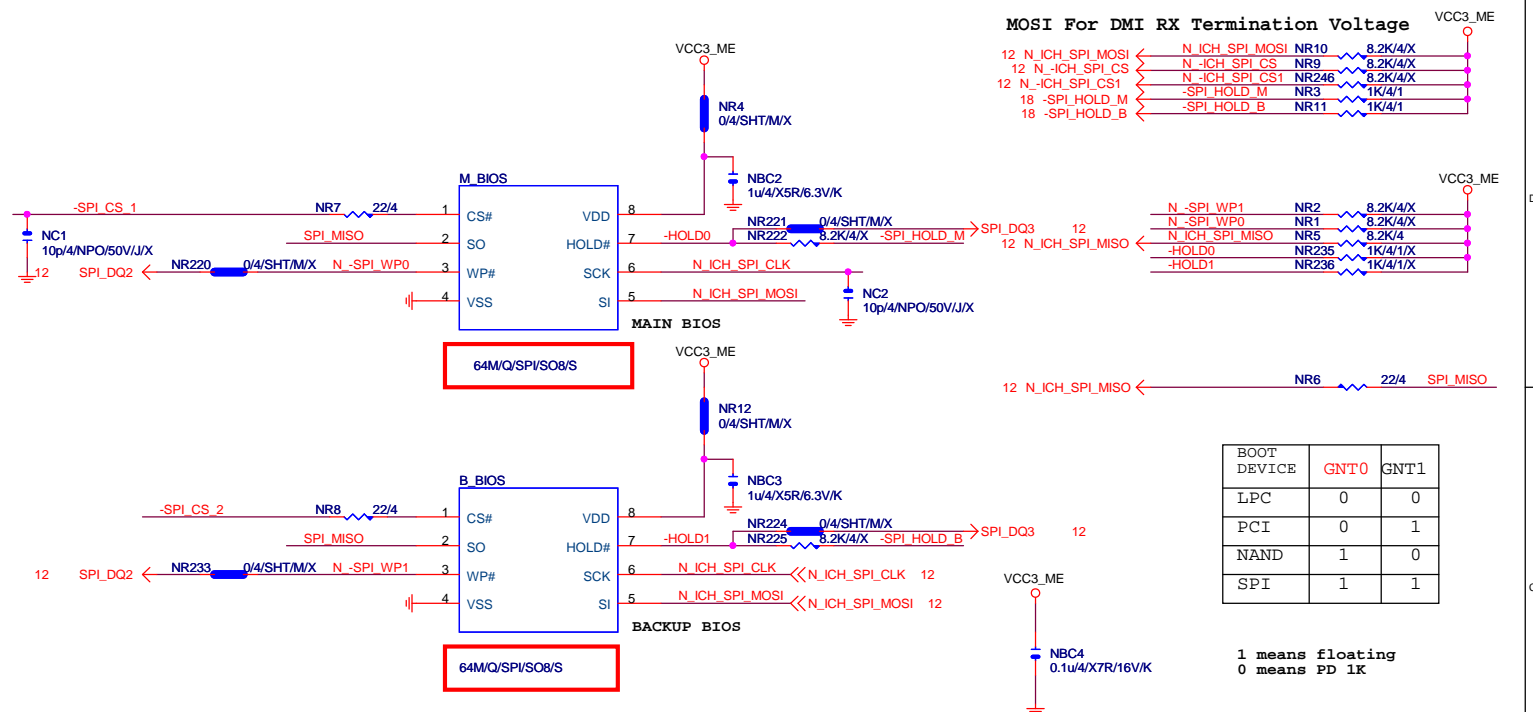
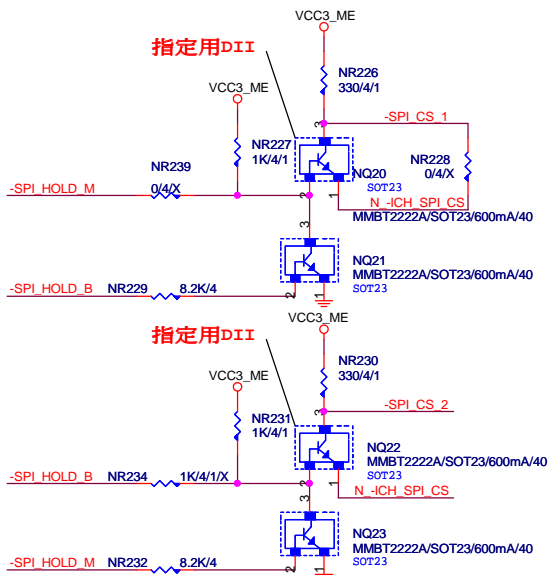
USB20 ESD PROTECT



Gigabyte Technology

Title		COM/ PROHOT/ R_USB	
Size	Document Number	GA-H97-DS3H	
Custom		Rev 1.0	
Date:	Monday, March 10, 2014	Sheet	19 of 34

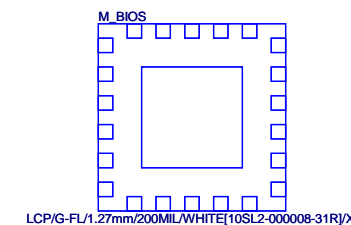
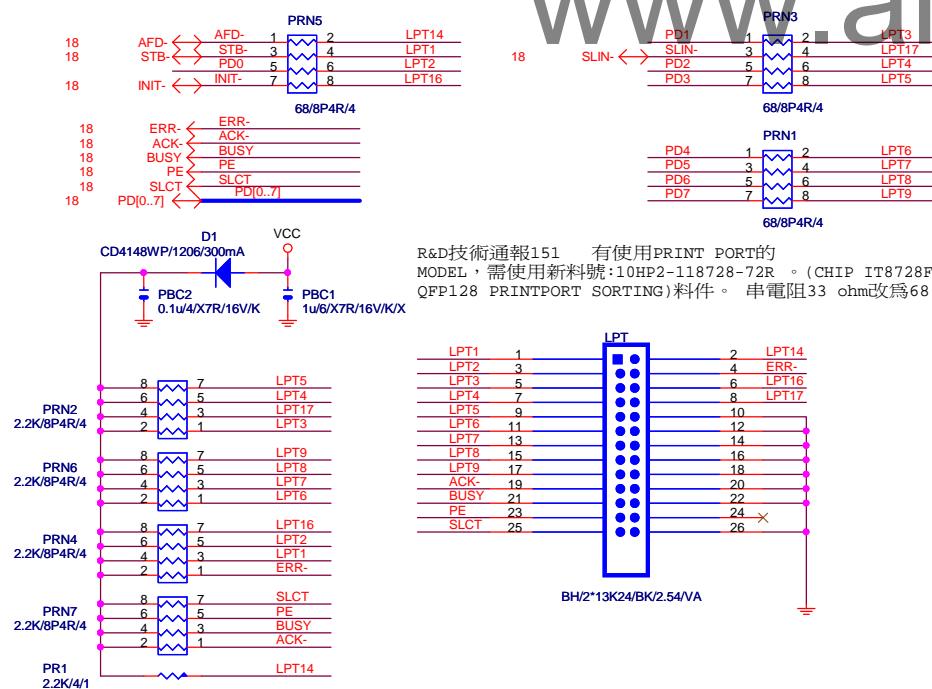
DUAL BIOS



BOOT DEVICE	GNT0	GNT1
LPC	0	0
PCI	0	1
NAND	1	0
SPI	1	1

1 means floating
0 means PD 1K

LPT PORT



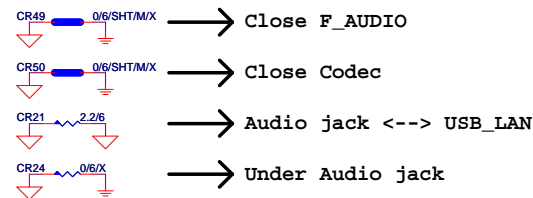
R&D技術通報151 有使用PRINT PORT的
MODEL, 需使用新料號:10HP2-118728-72R。(CHIP IT8728F/EX (GB) ITE/SMD
QFP128 PRINTPORT SORTING)料件。串電阻33 ohm改為68 ohm。

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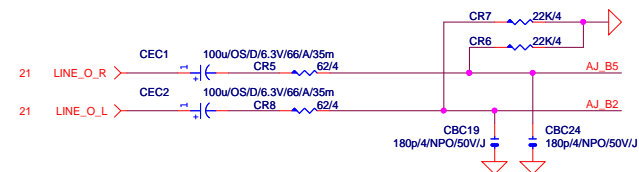
FOR ON/OFF PLAY



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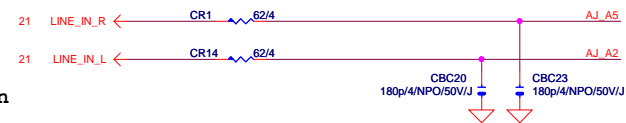
LINE-OUT



LINE-IN

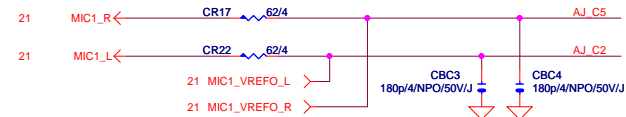
Verify MIC function
in LINE-in

Only reserved for ALC888



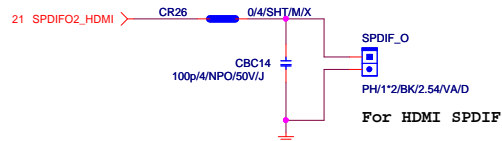
For 889A/888

MIC-IN



SURROUND

SPDIF_OUT

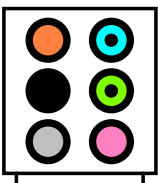


SPDIF_IN

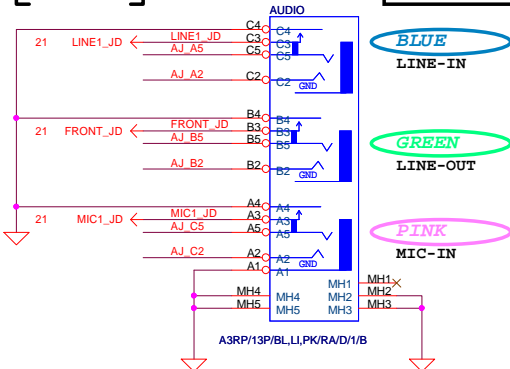
CEN/LFE

SURR BACK

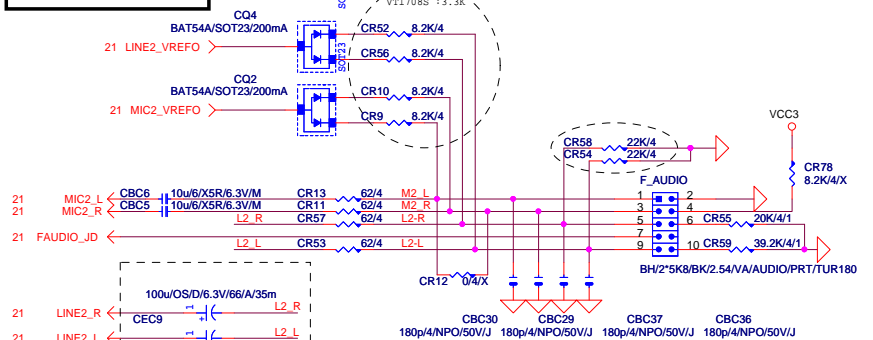
AZALIA JACK



AZALIA JACK



AZALIA FRONT PANEL

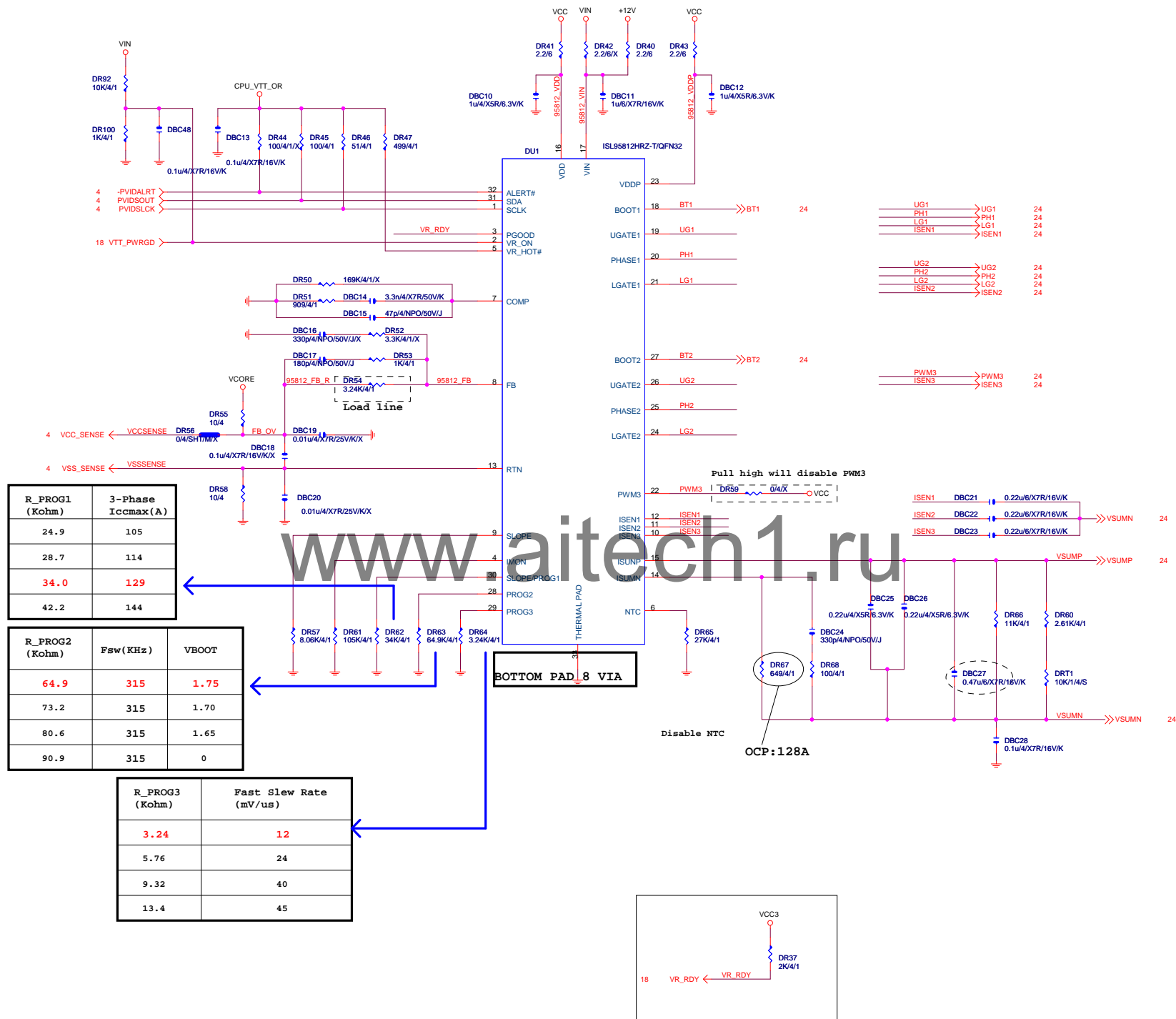


Gigabyte Technology

Title		
AUDIO JACK		
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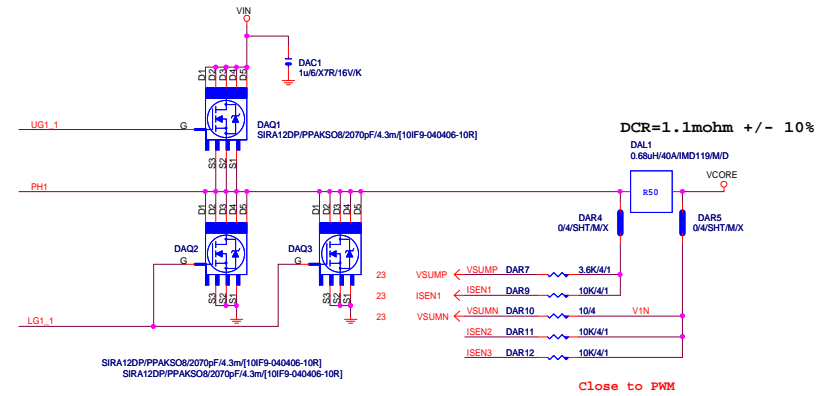
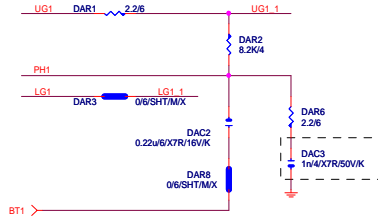
VCORE



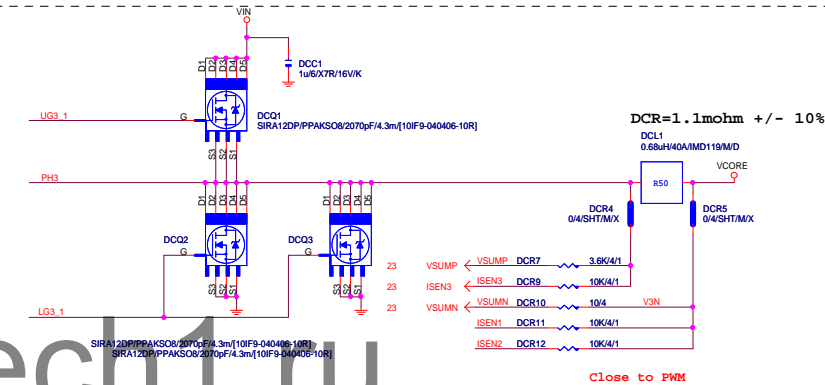
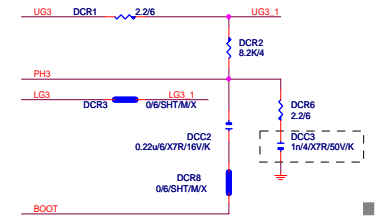
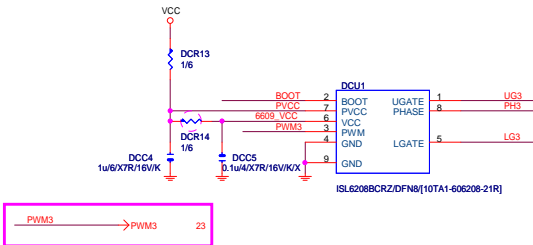
VCORE



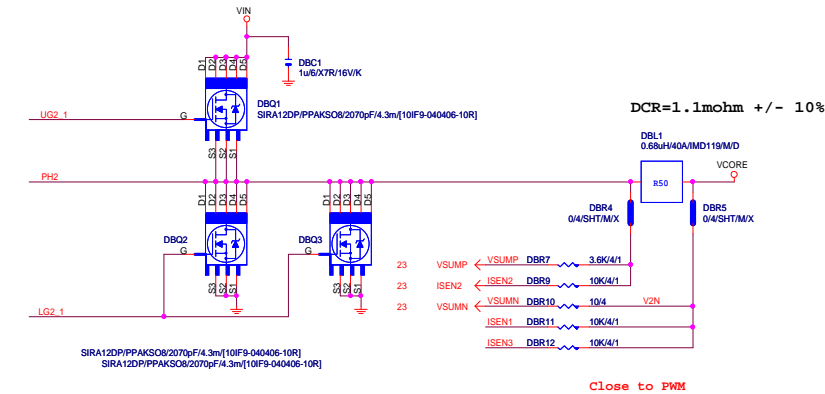
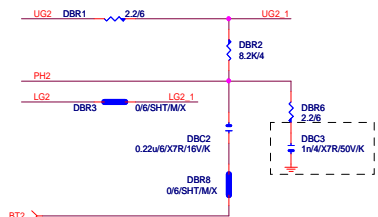
[1] 23



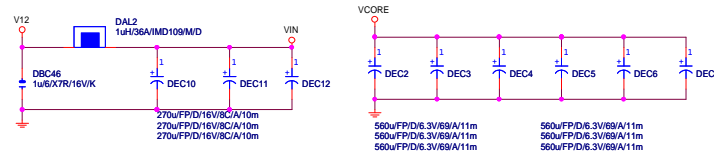
[3]



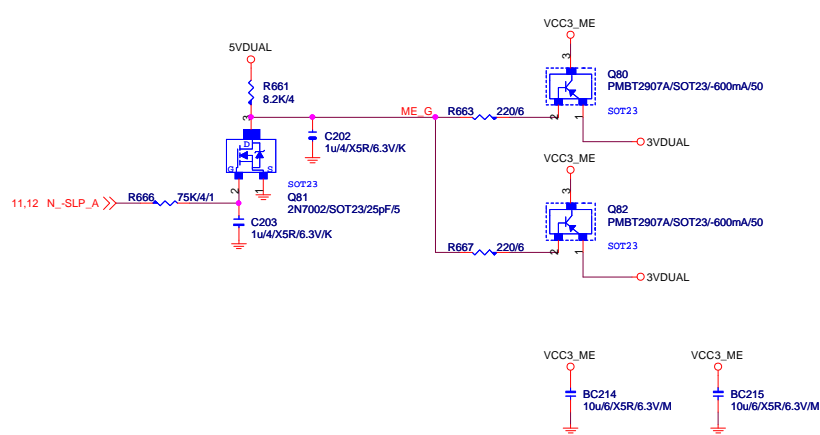
[2]



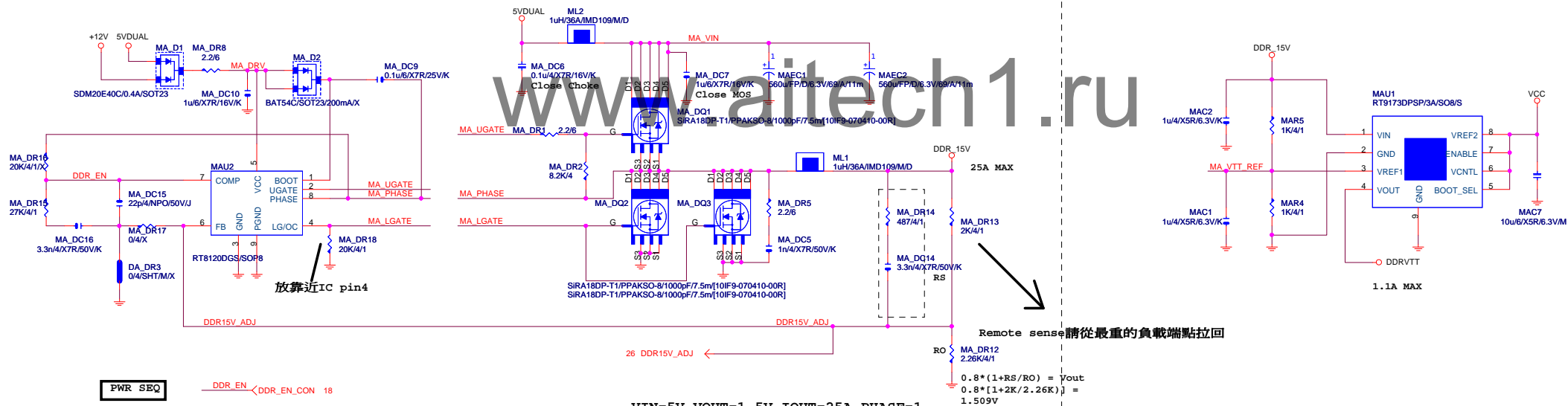
MOSFET HEATSINK



VCC3_ME



DDRVTT

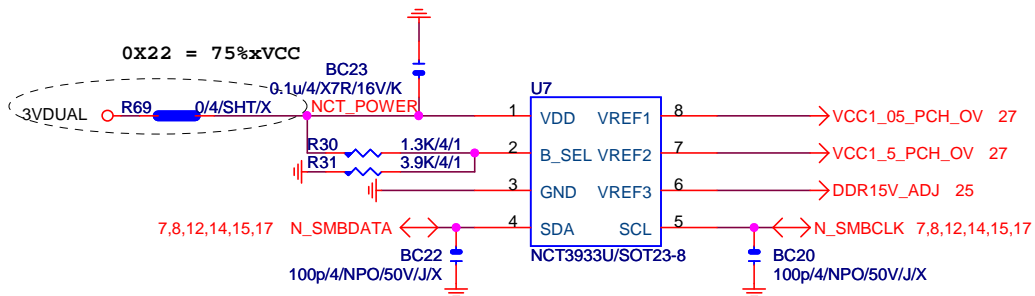


```
OCP:35.82A for Rds=6.7m for vishay@4.5V
OCP:72.727A for Rds=3.3m for renesas@10V
OCP:48A=Roset*Iocset / Rds(on)
      =12K*10uA / [5/5]
```

$$\begin{aligned} .8 * (1 + R_S / R_O) &= V_{out} \\ .8 * [1 + 2K / 2.26K] &= \\ .509V \end{aligned}$$

Remote sense請從最重的負載端點拉回

OVER VOLTAGE




0X2A = 0%xVCC

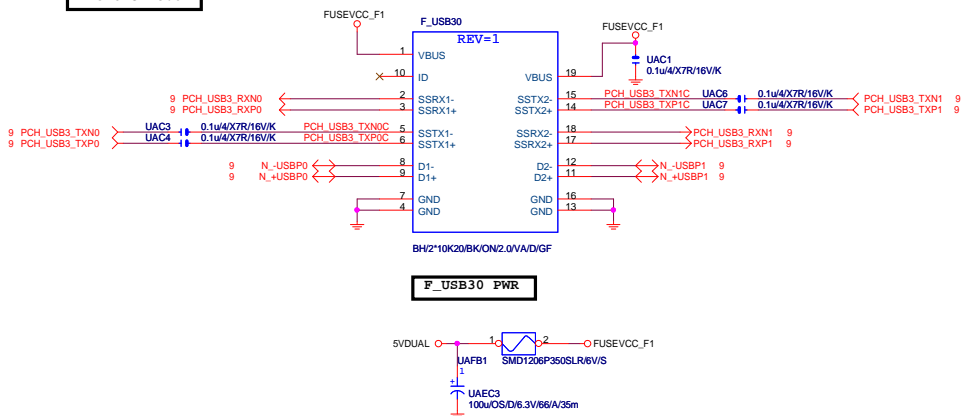
0X20 = 100%xVCC

NCT3933	0X2A	0X20	0X22
VREF1	DDRVTT	VREF_DDRA_DQ	PCH Core
VREF2	VREF_DDRA_CA	N/A	VCC1_5_PCH
VREF3	VREF_DDRA_CA	VREF_DDRB_DQ	SMREF

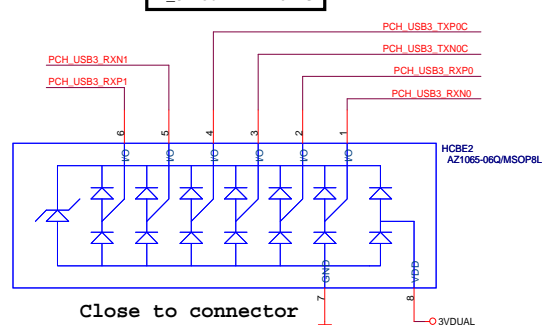
Gigabyte Technology

			
Title			
CPU CORE VR-2			
Size Custom	Document Number		Rev
	GA-H97-DS3H		1.0
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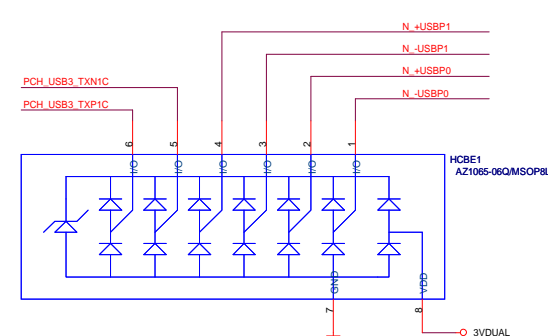
Front USB3.0



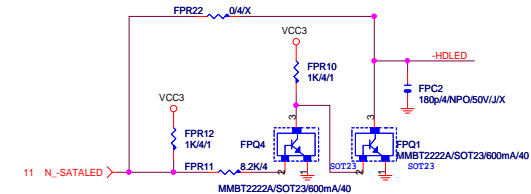
F_USB30 ESD PROTECT



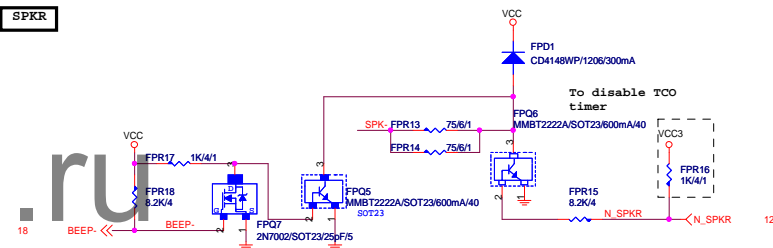
Close to connector



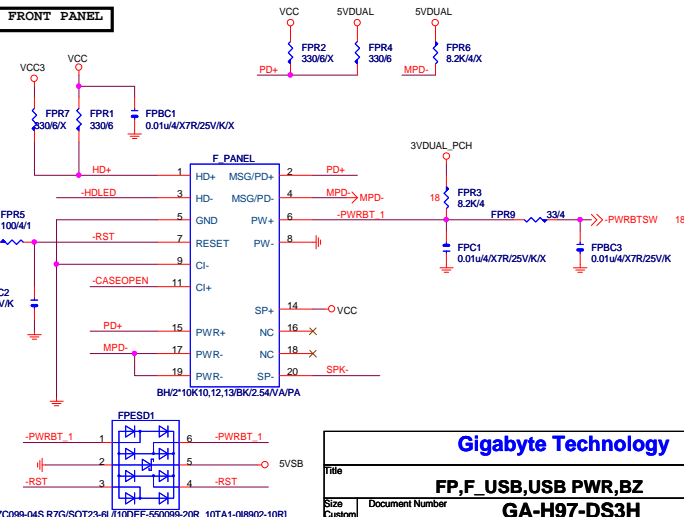
SATA LED



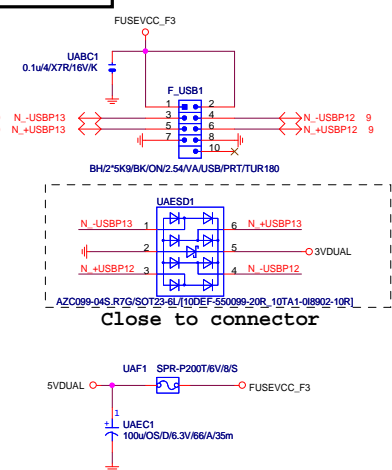
SPKR



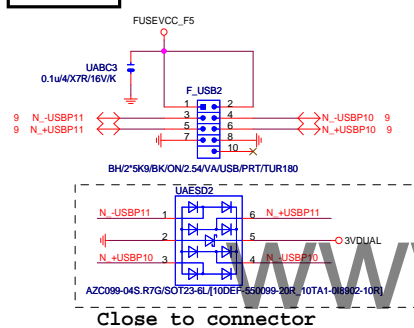
INTEL FRONT PANEL



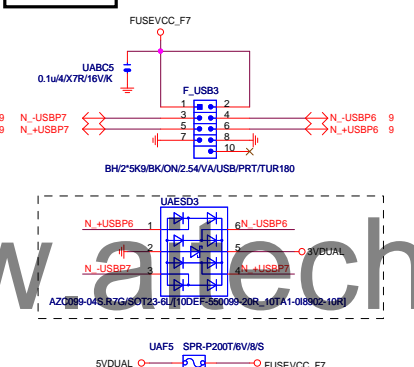
FRONT USB1



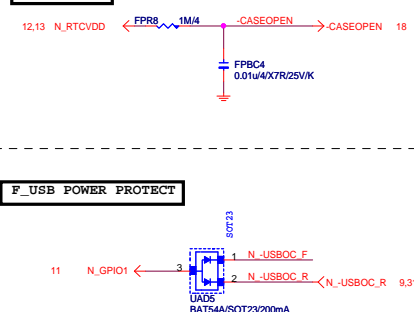
FRONT USB2



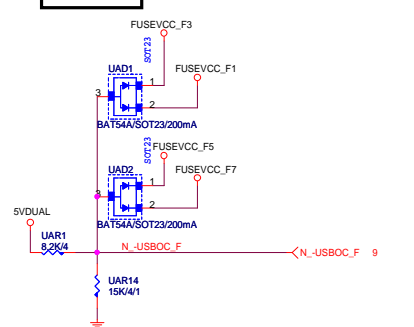
FRONT USB3



CASE OPEN



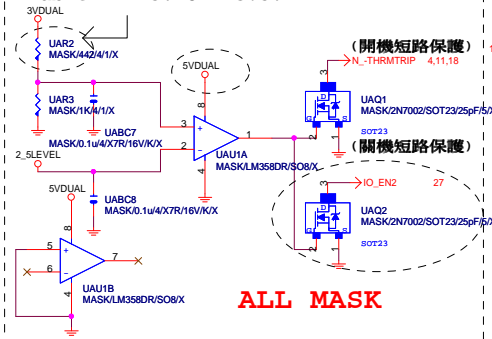
-USBOC_F



USB2.0 Signal & power short protection

USB2.0 Signal > 4.85V

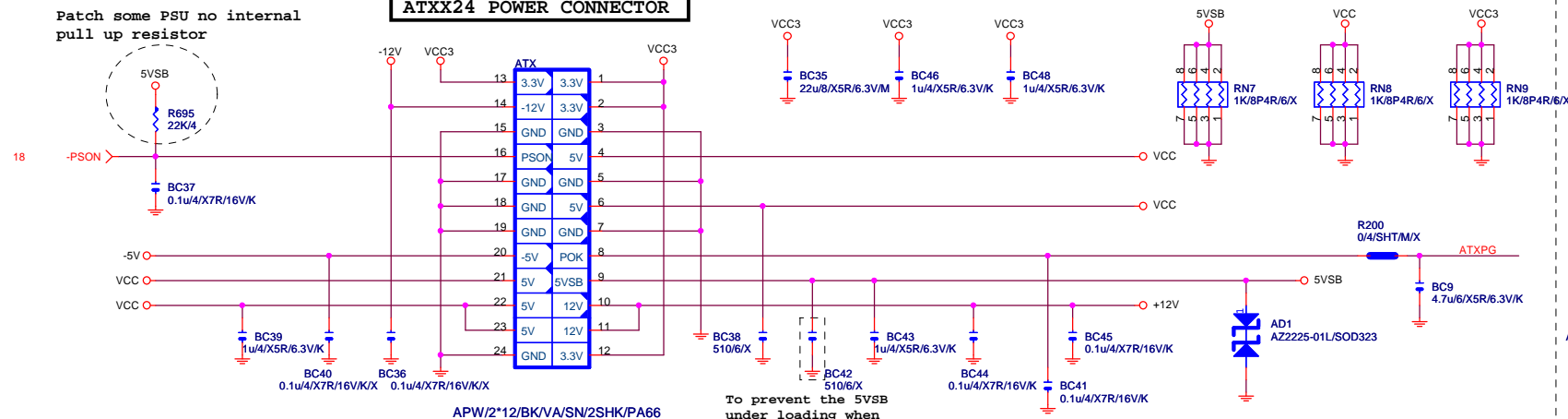
Enable --> 3VDUAL=3.5V



ALL MASK

Patch some PSU no internal pull up resistor

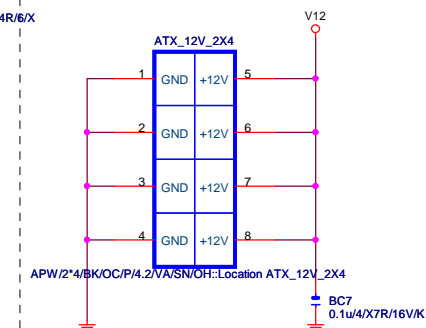
ATXX24 POWER CONNECTOR



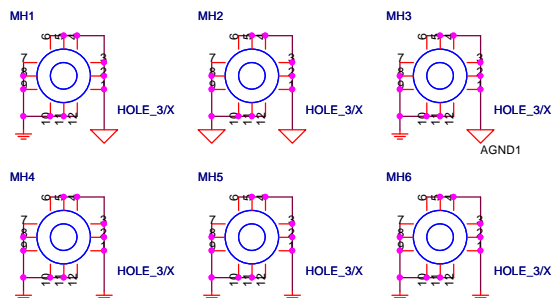
APW/2*12/BK/VA/SN/2SHK/PA66

To prevent the 5VSB under loading when boot

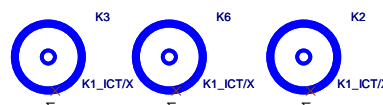
ATXX4 POWER CONNECTOR



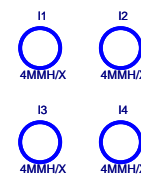
APW/2*4/BK/OC/P/4.2VA/SN/OH:Location ATX_12V_2X4



HOLE_4-RH-1



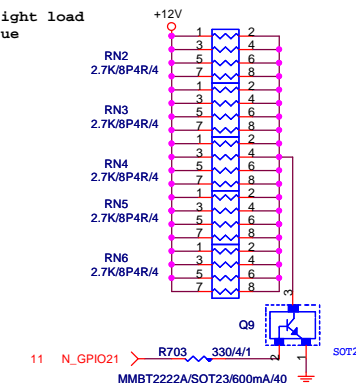
K1-ICT



4MMH

【技術通報R&D技術通報153】

To fix 12V light load abnormal issue



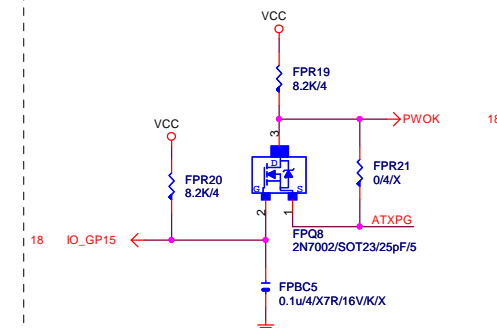
CLK GEN

CPU Frequency Selection

FSLB	FSLA	CPU
0	0	100M <Default>
0	1	133M
1	0	200M
1	1	166M

PWOK PATCH

【技術通報R&D技術通報154】



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ATX POWER CONNECTOR		
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Rev 0.2 modexy

VREF

OR73
10K/4/1

R674
8.2K/4

R675
8.2K/4

SYS_TEMP

CPU_TEMP

PCH_TEMP

OC7
1u4/XSR/6.3V/K

OC6
1u4/XSR/6.3V/K

RS_SYS
10K/1/4/S

Close S10

CLOSE CPU
VCORE NOS

12V VREF

OR2 10K/4/1

OR3 10K/4/1

OR85 10K/4/1

OC13 1u4/XSR/6.3V/K

OC14 1u4/XSR/6.3V/K

OC15 1u4/XSR/6.3V/K

RS1 100K/1/4/S

RS4 100K/1/4/S

RS_PWM 100K/1/4/S

126~133 degree

MOSFET1 1u4/XSR/6.3V/K

MOSFET2 1u4/XSR/6.3V/K

MOSFET3 100K/1/4/S

RS1、RS2、RS3 CLOSE CPU VR MOSFET

The diagram illustrates the power supply section of the TI9728B. It shows a network of resistors (OR75, OR74, OR57, OR79, OR78, OR81, OR70, OR10, OR12, OR53, OR3) and capacitors (OC9, OC8, OC4, OC10, OC11, OC3) connected to various input pins (VIN5, VIN6, VIN2, VIN1, VIN4) and output pins (VIN0, VINC0). The network is powered by VCORE, DDR_15V, VCC3, +12V, CPU_VAUXG, and VCC1. The diagram is divided into sections by dashed lines, with a large 'W' watermark on the right.

[illegible]

VCC

9.2K/4

R696

18 FANPWM1

R672 100K/4

+12V

R673 3.3K/4

R677 15K/4

R678 6.2K/4

FANIO1

EC6

1

100uF/16V/69/4/35m

C233 1uF/50V

Anti SPlke

BC220

0.1uF/4X7R/16V/K

CPU_FAN

FAN1*1.4W/4V/A3/PA66

[illegible][illegible][illegible]

Title			
HWM,KB/MS, FAN CTRL			
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LAN:INTEL I217

100歐姆:[20/4/8/4/20]

80歐姆:[15/5/5/15]

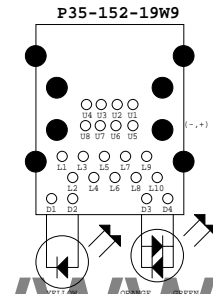
SRCCLK 50歐姆:[18/4/10/4/18]

離IC近越好

FOR DSM MODE
(DEEP SLEEP MODE)

Dual Color LED
D4 D3 Green
D4 D3 Orange

Single Color LED
D1 Yellow



USB30_LAN CONNECTOR

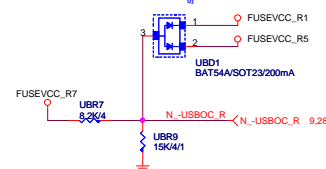
100歐姆:[20/4/8/4/20]

90歐姆:[15/5/7/5/15]

USB+LAN1G/GO,YOS/RA/D12/ES(11NR6-702009-96R)

5VDUAL 0.1u4/X7R/16V/K FUSEVCC_R1

-USBOC_R



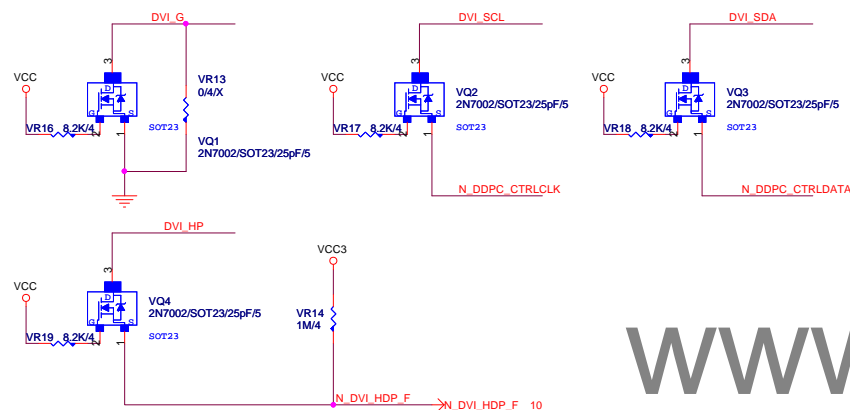
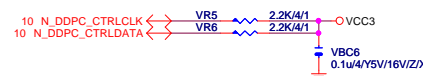
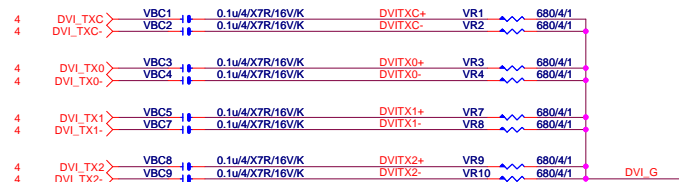
LAN

USB_LAN <--> R_USB30_1

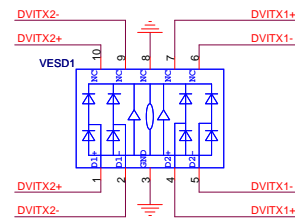
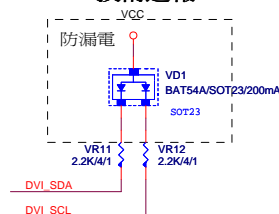
Gigabyte Technology			
INTEL LAN I217			
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Custom			
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www.aitech1.ru

DVI NON LEVEL SHIFT

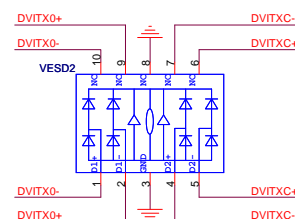


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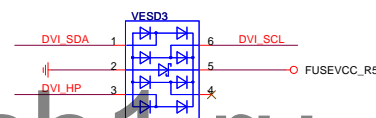
AZ1045-04F/MSOP10

Close to connector



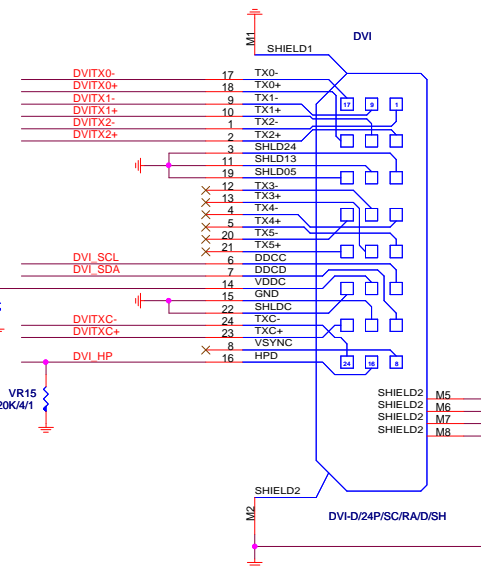
AZ1045-04F/MSOP10

Close to connector



AZC099-04S.RTG/SOT23-6L[10DEF-550093-20R_10T1-018902-10R]

Close to connector

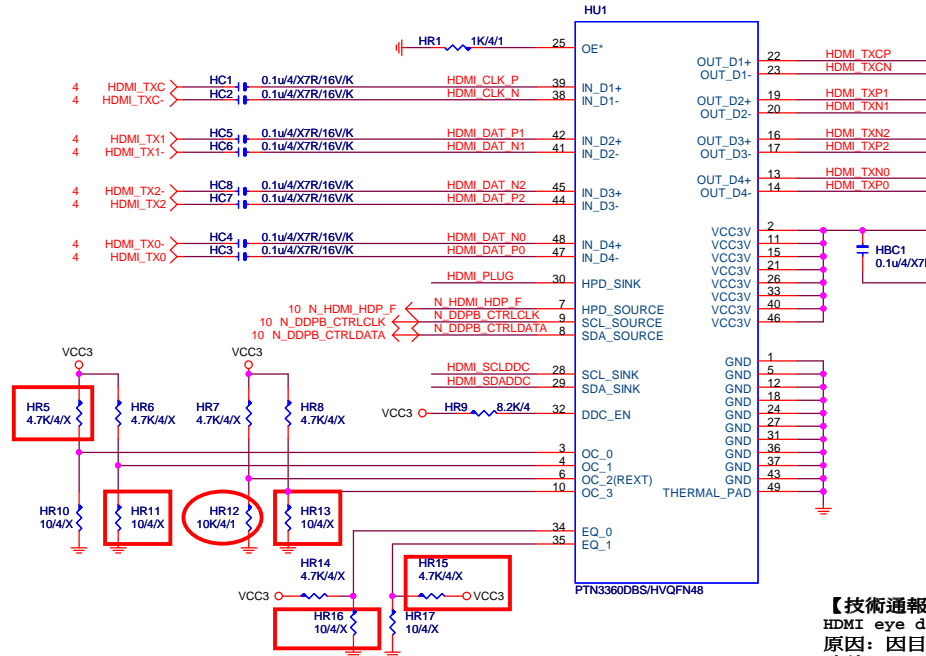


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DVI			
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HDMI LEVEL SHIFT

HDMI:20/4/6/4/20
Impedance=85 +- 17.5%



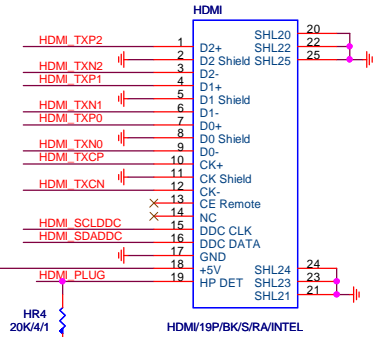
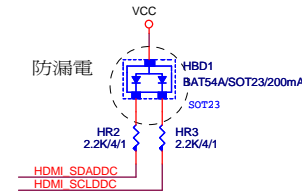
PTN3360:PIN 4/10/34/35 NC PIN,都不上值;只上HR12:10K
ASM1442:紅色框要上,HR12:3.16K

【技術通報R&D技術通報150】

HDMI eye diagram1.4版(deep color)會fail

原因: 因目前的HDMI訊號過長,造成RISING TIME過慢,而會壓到eye diagram

改善: ASMEDIA ASM1442 : 3.16K(PIN6 PULL DOWN電阻) 10ohm(PIN4 PULL DOWN電阻)



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HDMI			
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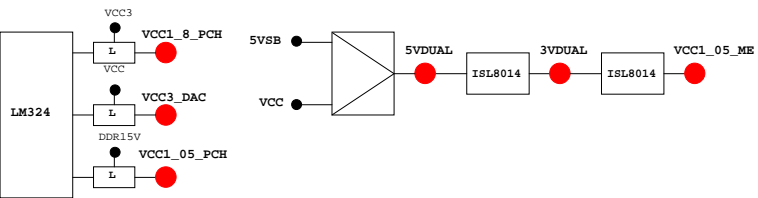
PCB GPIO LIST TABLE

PIN NAME	PWR	Default	USAGE	NOTE
GP0	MAIN	H-Z	GPIO0	N/A
GP1/TACH1	MAIN	GPI	GPIO1	N/A
GP2/PIRQE#	MAIN	GPI	~PIRQE	P/U 8.2K VCC3
GP3/PIRQF#	MAIN	GPI	~PIRQF	P/U 8.2K VCC3
GP4/PIRQG#	MAIN	GPI	~PIRQG	P/U 8.2K VCC3
GP5/PIRQH#	MAIN	GPI	~PIRQH	P/U 8.2K VCC3
GP6/TACH2	MAIN	GPI	PCIE1 Detect	P/U 8.2K VCC3
GP7/TACH3	MAIN	MAIN	GPIO7	P/U 8.2K VCC3
GP8	STBY	H	GPIO8	N/A
GP9/OC5#	STBY	NATIVE	USB OC5#	N/A
GP10/OC6#	STBY	NATIVE	USB OC6#	N/A
GP11/SMBALERT#	STBY	NATIVE	USB PWR protect	P/U 8.2K 3VDUAL
GP12	STBY	L	GPI	GPIO12
GP13	STBY	L	GPI	LPCPME#
GP14/OC7#	STBY	NATIVE	USB OC7#	N/A
GP15	STBY	L	GPI	GPIO15(TLS Enable)
GP16	MAIN	GPI	GPIO16	P/U 8.2K VCC3
GP17/TACH0	MAIN	GPI	GPIO17	P/U 8.2K VCC3
GP18	MAIN	GPI	Mobile Only	N/A
GP19	MAIN	GPI	GPIO19	P/U 8.2K VCC3
GP20	MAIN	GPI	GPIO20	P/U 8.2K VCC3
GP21	MAIN	GPI	GPIO21	P/U 8.2K VCC3
GP22	MAIN	H-Z	GPI	GPIO22
GP23	MAIN	GPI	GPIO23	N/A
GP24	STBY	L	GPI	SKTOCC#
GP25	STBY		Mobile Only	N/A
GP26	STBY		Mobile Only	N/A
GP27	STBY	H	GPO	GPIO27
GP28	STBY	H	GPO	PWR LED
GP29	STBY	L	GPI	GPIO29
GP30	STBY	H-Z	GPI	Mobile Only
GP31	STBY	H-Z	GPI	Mobile Only
GP32	MAIN	H	GPO	N/A
GP33	MAIN	H	GPO	N/A
GP34	MAIN	H-Z	GPI	-PCI_STOP
GP35	MAIN	L	GPO	-ACZ_DET
GP36	MAIN	GPI	N/A	N/A
GP37	MAIN	GPI	N/A	N/A
GP38	MAIN	H-Z	GPI	PCIEX4 Detect
GP39	MAIN	H-Z	GPI	GPIO39
GP40	STBY	NATIVE	USB OC1#	N/A
GP41	STBY	NATIVE	USB OC2#	N/A
GP42	STBY	NATIVE	USB OC3#	N/A
GP43	STBY	NATIVE	USB OC4#	N/A
GP44	STBY	L	NATIVE	GPIO44
GP45	STBY	NATIVE	GPIO45	P/U 8.2K 3VDUAL
GP46	STBY	L	NATIVE	GPIO46
GP47	STBY		Mobile Only	N/A
GP48	MAIN	H-Z	IN	GPIO48
GP49	MAIN	H-Z	IN	GPIO49
GP50	MAIN	NATIVE	-REQ1	P/U 2.2K VCC
GP51	MAIN	H	NATIVE	-GNT1
GP52	MAIN	NATIVE	-REQ2	P/U 2.2K VCC
GP53	MAIN	H	NATIVE	-GNT2
GP54	MAIN	NATIVE	-REQ3	P/U 2.2K VCC
GP55	MAIN	H	NATIVE	-GNT3
GP56	STBY	NATIVE	Mobile Only	N/A
GP57	STBY	H-Z	IN	VCORE_OV1
GP58	STBY	H-Z	NATIVE	F_USB_OC
GP59	STBY	NATIVE	USB_OC0#	N/A
GP60	STBY	H-Z	NATIVE	N/A(Reverse)
GP61	STBY	L	NATIVE	-SUSTAT
GP62	STBY	L	NATIVE	SUSCLK
GP63	STBY	L	NATIVE	GPIO63
GP64	MAIN	L	NATIVE	CLKOUTFLEX0
GP65	MAIN	L	NATIVE	CLKOUTFLEX1
GP66	MAIN	L	NATIVE	CLKOUTFLEX2
GP67	MAIN	L	NATIVE	CLKOUTFLEX3
GP72	STBY	H-Z	NATIVE	VCORE_OV4
GP73	STBY		Mobile Only	N/A
GP74	STBY	H-Z	NATIVE	1_05V_OV2
GP75	STBY	H-Z	NATIVE	N/A(Reverse)

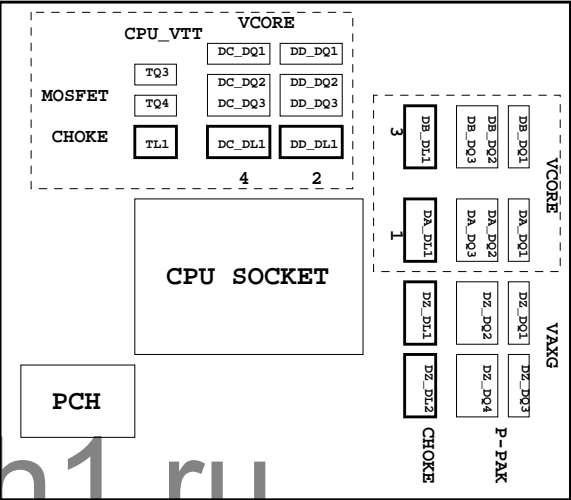
Super I/O ITE8720 GPIO Table

PIN NAME	USAGE	NOTE
SVC/PECI_RQT/GP14	-PECI_REQ	
PWROK1/GP13	PWROK1/ITE_PWROK	
KRST#/GP62	-KBRST	
SO/GP50	-ICH_SPI_CS	
IRTX/GP47/CE2_N/JP7	CEB_N	
GP46/IRRX	-LAN2_DSM	
PSION#/GP42	-PSON	
PWROK2#/GP41	PECI_CTL	
PCIRST3#/GP10/VDIMM_STR_EN	-PCIE_RST	
RSMRST#CIRRX1/GP55	-RSMRST	
PME#/GP54	-LPCPME	
PD5/GP75/BUSS00	N/A	

PIN NAME	USAGE	NOTE
FAN_TAC2/GP52	FANIO2	
FAN_TAC3/GP37	FANIO3	
VIDO3/FAN_TAC4/GP25/DSR2#	FANIO4	
FAN_CTL2/GP51	FANPWM2	
FAN_CTL3/GP36	FANPWM3	
VID4/GP34	BEEP-	
VID3/GP33	TURBO1	
VID2/GP32	TURBO0	
VCORE_GOOD/VID6/GP63	CPUT_LED1_C	
VID5/GP35	CPUT_LED2_C	
VID1/GP31	CPUT_LED3_C	
VID0/GP30	-LAN1_DSM	NBT_LED1_C
SLCT/GP80	CPU_LED1_C	
PE/GP81	CPU_LED2_C	
BUSY/GP82	CPU_LED3_C	
PD3/GP73/BUSS11	SB_LED1_C	
PD4/GP74/BUSS12	SB_LED2_C	
VCORE_EN/VID7/GP64	IT_GP64	SB_LED3_C
PD0/GP70	NB_LED1_C	
PD1/GP71	NB_LED2_C	
PD2/GP72/BUSS10	NB_LED3_C	
GP22/SEN	LOW_PWR_1	
VID05/GP27/SEN2	LOW_PWR_2	
PCIRST2#/GP11	-PFMRST1	
PCIRST1#/GP12	-PFMRST2	
3VSB5W#/GP40	CSI_F0	BSEL166_1
SUSCH#/GP53	CSI_F1	BSEL166_2
GP23/SI	BSEL166_3/CSISBSL	
VID00/GP20/CTS2#	CPUT_LED1_C	BSEL166_4
GP65/VDDA_EN/GB_01	MB_ID2	
PD6/GP76/BUSS01	MB_ID3	
PD7/GP77/BUSS02	MB_ID4	
AFD#/GP86/SMBC_R	PIN	FST_2X8
INIT#/GP85/SMBC_M	SEC_2x8	GTLREF_AD2
ACK#/GP83	DDR_LED1_C	
VID01/GP21/DCD2#	DDR_LED2_C	
STB#/GP87/SMBC_M	DDR_LED3_C	
PWRON#GP44	VCORE_OV1	
PANSWH#/GP43	PWRBTSW	
KDAT/GP61	-PWRBTSW	
KCLK/GP60	KDAT	
MDAT/GP57	KCLK	
MACL/GP56	MDAT	
GP66/VLDT_EN/GB_02	NBT_LED1_C	MCLK
SVD/PCIRSTIN#/CIRT2/GP15	PWM2_CR	
KDAT/GP61	PWM2_CR	
GP67/CPU_PG/GB_03	EN_LOADLINE	IT_GP67/-EN_PWM2
SLIN#/GP84/SMBC_R	-EN_PWM2	
PSI_L/FAN_CLT5/CIRRX2/GP16	-THERM	
VID04/GP26/SOUT2	DDR18V_PH2_EN	
VID02/FAN_TAC5/GP24/DSR2#	DDR18V_LED	
VID06/GP17/RI2#	1_1V_PH_EN	
VID07/JP6/DTR2#	JP6	
PD5/GP75/BUSS00	SB_LED3_C	



PWM各相位的擺法如下：



BIOS超電壓對應表：

線路圖名稱	BIOS選項
Vcore	CPU Vcore
CPU_VTT	CPU Termination
CPU_VAXG	CPU Graphic Core
VCC1_8_PCH	CPU PLL
VCC1_05_PCH	PCH core
3VDUAL	3VDUAL
DDR15V	DRAM voltage
DDRVTT	DRAM Termination
VREF_CA_A/VREF_CA_B	DRAM Address Ref
VREF_DQ_A/VREF_DQ_B	DRAM Data Ref

散熱模組料號：

Z77-D3H :
PCH :
12SP2-S05511-01R/02R/03R
MOSFET :
12SP2-S08924-01R/02R/03R

	3 pin FAN control	4 pin FAN control	FAN speed	Controller
CPU FAN	FANPWM1	FANPWM3	FANIO1	IT8720
	ICH_FAN_PWM2	ICH_FAN_PWM0	ICH_FAN_TACH0	PCH
SYS FAN	FANPWM2	N/A	FANIO2	IT8720
	ICH_FAN_PWM1	N/A	ICH_FAN_TACH1	PCH
PWR FAN	N/A	N/A	FANIO3	IT8720
			ICH_FAN_TACH2	PCH

Gigabyte Technology			
TABLE LIST			
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